PALM INTRANET

Inventor Name Search Result

Your Search was:

Last Name = PHILLIPS
First Name = BRENT

Application#	Patent#	Status	Date Filed	Title	Inventor Name
08799293	Not Issued	161	02/13/1997	VIRTUAL REAL TIME INTERACTION OVER WIDE-AREA NETWORKS	PHILLIPS, BRENT
60011651	Not Issued	159	02/14/1996	VIRTUAL REAL TIME INTERACTION OVER WIDE-AREA NETWORKS	PHILLIPS, BRENT
10676858	7114582	150	10/01/2003	METHOD AND APPARATUS FOR REMOVING CUTTINGS FROM A DEVIATED WELLBORE	PHILLIPS, BRENT E.
60416020	Not Issued	159	10/04/2002	Method and apparatus for removing cuttings from a deviated wellbore	PHILLIPS, BRENT E.
10856391	Not Issued	41	05/28/2004	Multi-column multi-data type internationalized sort extension method for web applications	PHILLIPS, BRENT R.
10856392	Not Issued	41	05/28/2004	Multi-column multi-data type internationalized sort extension method for web applications	PHILLIPS, BRENT R.
09975229	6799186	150	10/11/2001	SLA MONITOR CALENDAR BUFFERING	PHILLIPS, BRENT R.
10004923	6961908	150	12/05/2001	SYSTEM AND METHOD FOR NAVIGATING GRAPHOCAL IMAGES	PHILLIPS, BRENT RUSSEL
10165129	6920449	150	06/06/2002	BEAN GENERATED SQL FOR BACK-END MANAGEMENT AND CONFIGURATION	PHILLIPS, BRENT RUSSEL
10210362	Not Issued	123	07/31/2002	Graphical user interface toolkit for generating swing screens	PHILLIPS, BRENT RUSSEL
10692166	Not Issued	71	10/23/2003	Method and system for generating SQL joins to optimize performance	PHILLIPS, BRENT RUSSEL
10756932	Not	71	01/14/2004	Method and apparatus for	PHILLIPS, BRENT

	Issued			validating and configuring database transaction requests from multiple clients	RUSSEL
10803630	Not Issued	71	03/18/2004	Method and apparatus for splitting and merging request and response data at runtime	PHILLIPS, BRENT RUSSEL
10803658	Not Issued	93	03/18/2004	METHOD AND APPARATUS FOR GENERATING QUERY AND RESPONSE STATEMENTS AT RUNTIME FROM GENERIC REQUESTS	PHILLIPS, BRENT RUSSEL
11032849	Not Issued	30	01/11/2005	Dynamic source code analyzer	PHILLIPS, BRENT RUSSEL
11291542	Not Issued	25	12/01/2005	User/process runtime system trace	PHILLIPS, BRENT RUSSEL
11291548	Not Issued	30	12/01/2005	Efficient, centralized management of application log configuration settings	PHILLIPS, BRENT RUSSEL
10660289	Not Issued	30	09/11/2003	Methods, systems, and media to enhance persistence of a message	PHILLIPS, BRENT RUSSELL
08350474	Not Issued	166	12/07/1994	MULTIPURPOSE CONVERTIBLE BED COMFORTER	PHILLIPS, BRENT S.
08907400	5887299	150	08/07/1997	MULTI-PURPOSE CONVERTIBLE BED COMFORTER	PHILLIPS, BRENT S.
11470077	Not Issued	19	01/01/0001	Tree-Based Information Query Model	PHILLIPS, BRENTON ASHER

Inventor Search Completed: No Records to Display.

Search Another: Inventor

Last Name	First Name	
PHILLIPS	BRENT	Search

To go back use Back button on your browser toolbar.

Back to PALM | ASSIGNMENT | OASIS | Home page

Set	Items	Description
S1	343309	QUEUE? ? OR FIFO OR FIRST(1X)FIRST OR BUFFER? ?
S2	37238	(VOLATILE OR (NON OR "NOT")()PERSISTENT)(2W)(STRUCTURE? ? - OR MEMORY OR STORE OR STORAGE)
S3	35832	(NON()VOLATILE OR PERSIST???)(2W)(S1 OR STRUCTURE? ? OR ME-MORY OR STORE OR STORAGE)
S4	206476	(READ()ONLY OR FLASH)()MEMORY OR ROM OR PROM OR EPROM OR E-EPROM OR FPO OR EDO OR FIRMWARE
S5	74795	(COPY??? OR COPIE? ? OR REPLICAT? OR DUPLICAT? OR CLON??? - OR MIRROR? OR BACKUP OR BACK???()UP)(5N)(MESSAGE? ? OR PACKET? ? OR FRAME? ? OR DATA OR INFORMATION OR CONTENT? ? OR BYTE? ? OR BIT? ? OR CHARACTER? ? OR STRING? ? OR WORD? ?)
S6	344303	(TRANSFER????? OR WRITE? ? OR WRITING OR WRITTEN OR SAV??? OR STORE? ? OR STORING)(5N)(MESSAGE? ? OR PACKET? ? OR FRAME? ? OR DATA OR INFORMATION OR CONTENT? ? OR BYTE? ? OR BIT? ? OR CHARACTER? ? OR STRING? ? OR WORD? ?)
S7	864397	RECOVER??? OR RESTOR? OR CRASH??? OR FAIL??? OR FAULT? ? OR MIRROR? OR BACKUP OR BACK???()UP OR LOSS
S8	43196	S5:S6(5N)S1:S2
S9	2398	S8(7N)(DISK OR DRIVE OR DISC)
S10	312	S9(100N)S7
S11	5953	S5:S6(7N)QUEUE? ?
S12	196	S11(7N)(DISK OR DRIVE OR DISC OR S3:S4)
S13	121	S11(7N)(DISK OR DRIVE OR DISC)
S14	53	S12(100N)S7
S15	36	S14 AND AC=US/PR AND AY=(1978:2003)/PR
S16	36	S14 AND AC=US AND AY=1978:2003
S17	36	S14 AND AC=US AND AY=(1978:2003)/PR
S18	35	S14 AND PY=1978:2003
S19	44	S15:S18
S20	44	IDPAT (sorted in duplicate/non-duplicate order)

20/3,K/1   (Item 1 from file: 348)

02147587
Email using queues in non-presistent memory
E-Mail mit Warteschlangen in einem nicht-persistenten Speicher
**Courrier electronique utilisant les files d'attente dans une memoire non
    permanente**
PATENT ASSIGNEE:
  Strongmail Systems, Inc., (4968580), 11965 Venice Boulevard, Suite 209,
    Los Angeles, CA 90066, (US), (Applicant designated States: all)
INVENTOR:
  ADDANTE, Frank, 517 29th Street, San Francisco, California 94131, (US)
  MCQUILLEN, Tim, 513 Breakwater Drive, Redwood Shores, California 94065,
    (US)
LEGAL REPRESENTATIVE:
  Jones Day (102151), Rechtsanwalte, Attorneys-at-Law, Patentanwalte
    Prinzregentenstrasse 11, 80538 Munchen, (DE)
PATENT (CC, No, Kind, Date):   EP 1715641 A2 061025 (Basic)
                               EP 1715641 A3 061129
APPLICATION (CC, No, Date):   EP 2006013041 040211;
PRIORITY (CC, No, Date): US 449301 P 030220
DESIGNATED STATES: AT; BE; BG; CH; CY; CZ; DE; DK; EE; ES; FI; FR; GB; GR;
  HU; IE; IT; LI; LU; MC; NL; PT; RO; SE; SI; SK; TR
EXTENDED DESIGNATED STATES: AL; LT; LV; MK
RELATED PARENT NUMBER(S) - PN (AN):
  EP 1599805 (EP 2004710291)
INTERNATIONAL CLASSIFICATION (V8 + ATTRIBUTES):
IPC + Level Value Position Status Version Action Source Office:
  H04L-0012/58   A I F B 20060101 20060904 H EP
  H04L-0012/56   A N L B 20060101 20061025 H EP
ABSTRACT WORD COUNT: 148
NOTE:
  Figure number on first page: 1

LANGUAGE (Publication,Procedural,Application): English; English; English
FULLTEXT AVAILABILITY:
Available Text Language   Update   Word Count
      CLAIMS A (English)   200643   558
      SPEC A   (English)   200643   4118
Total word count - document A   4677
Total word count - document B   0
Total word count - documents A + B   4677

  ...SPECIFICATION mail may be assigned with a unique ID formed from a bit
  vector of the **queued** e-mail. The **bit** vector may be **stored** in
  persistent storage . The **bit** vector may include sufficient
  information to reconstruct the message and the state of processing of...

  ...of the e-mails. This information is stored on disk or persistent
  storage. This enables **recovering** the entire state of processing of the
  system, without storing the entirety of the e...

20/3,K/4   (Item 4 from file: 348)

00451969
MULTIPLE FACILITY OPERATING SYSTEM ARCHITECTURE
BETRIEBSSYSTEMAUFBAU MIT MEHREREN VERARBEITUNGSEINHEITEN
**ARCHITECTURE D'UN SYSTEME D'EXPLOITATION COMPRENANT PLUSIEURS UNITES DE
    TRAITEMENT**
PATENT ASSIGNEE:

AUSPEX SYSTEMS, INC., (1348490), 2952 Bunker Hill Lane, Santa Clara, CA
  95054, (US), (applicant designated states:
    AT;BE;CH;DE;DK;ES;FR;GB;IT;LI;LU;NL;SE)
INVENTOR:
  HITZ, David, 925 Wolfe Road Apartment 23, Sunnyvale, CA 94086, (US)
  SCHWARTZ, Allan, 12241 Marilla Drive, Saratoga, CA 95070, (US)
  LAU, James, 11570 Upland Way, Cupertino, CA 95014, (US)
  HARRIS, Guy, 707 Continental Circle Number 1237, Mountain View, CA 94040,
    (US)
LEGAL REPRESENTATIVE:
  Barnard, Eric Edward et al (28021), BROOKES & MARTIN High Holborn House
    52/54 High Holborn, London WC1V 6SE, (GB)
PATENT (CC, No, Kind, Date):   EP 490980 A1 920624 (Basic)
                               EP 490980 A1 930421
                               EP 490980 B1 990506
                               WO 9104540 910404
APPLICATION (CC, No, Date):   EP 90914006 900820; WO 90US4701 900820
PRIORITY (CC, No, Date): US 404885 890908
DESIGNATED STATES: AT; BE; CH; DE; DK; ES; FR; GB; IT; LI; LU; NL; SE
INTERNATIONAL PATENT CLASS (V7): G06F-015/16; G06F-015/76; G06F-013/00;
NOTE:
  No A-document published by EPO
LANGUAGE (Publication,Procedural,Application): English; English; English
FULLTEXT AVAILABILITY:
Available Text Language   Update   Word Count
  CLAIMS B (English)   9918   945
  CLAIMS B (German)   9918   741
  CLAIMS B (French)   9918   1267
  SPEC B   (English)   9918   20729
Total word count - document A   0
Total word count - document B   23682
Total word count - documents A + B 23682

...SPECIFICATION Note, the limit of 10 is set due to the size of a S
  facility **message** . Like the sector read/ **write**   **message** , this **message**
  is also inserted in a **drive** elevator **queue** first.
    If no error conditions are detected from the SCSI drive(s), this
  message is completed normally. When an error is detected, a data **recover**
   action is started. When there is a permanent drive error that prevents
  error **recovery** action from continuing, an error status code is reported
  as completion.
    K(underscore)reply or...

 20/3,K/6   (Item 6 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
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02018194
Secure transaction management
Gesicherte Transaktionsverwaltung
Gestion de transactions securisees
PATENT ASSIGNEE:
  Intertrust Technologies Corp., (2434323), 955 Stewart Drive, Sunnyvale,
    CA 94085, (US), (Applicant designated States: all)
INVENTOR:
  Ginter, Karl L., 10404 43rd Avenue, Beltsville, MD 20705, (US)
  Shear, Victor H., 5203 Battery Lane, Bethesda, MD 20814, (US)
  Sibert, W. Olin, 30 Ingleside Road, Lexington, MA 02173-2522, (US)
  Spahn, Francis J., 2410 Edwards Avenue, El Cerrito, CA 94530, (US)
  Van Wie, David M., 51430 Willamette Street, 6 Eugene, OR 97401, (US)
LEGAL REPRESENTATIVE:
  Beresford, Keith Denis Lewis (28273), BERESFORD & Co. 16 High Holborn,
    London WC1V 6BX, (GB)
PATENT (CC, No, Kind, Date): EP 1621960 A2 060201 (Basic)

APPLICATION (CC, No, Date):   EP 2005076129 970829;
PRIORITY (CC, No, Date): US 706206 960830
DESIGNATED STATES: AT; BE; CH; DE; DK; ES; FI; FR; GB; GR; IE; IT; LI; LU;
   MC; NL; PT; SE
RELATED PARENT NUMBER(S) - PN (AN):
   EP 922248 (EP 97939670)
INTERNATIONAL CLASSIFICATION (V8 + ATTRIBUTES):
IPC + Level Value Position Status Version Action Source Office:
   G06F-0001/00   A I F B 20060101 20051208 H EP
ABSTRACT WORD COUNT: 51
NOTE:
   Figure number on first page: 70

LANGUAGE (Publication,Procedural,Application): English; English; English
FULLTEXT AVAILABILITY:
Available Text Language   Update   Word Count
      CLAIMS A (English)   200605   249
      SPEC A   (English)   200605   180527
Total word count - document A   180776
Total word count - document B   0
Total word count - documents A + B 180776

   ...SPECIFICATION referenced in a PERC 808 and/or "user rights table" 464)
   597(4), an event **queue** 597(57), and one or more fields 598 that
   cross-reference particular event codes with...

 20/3,K/7   (Item 7 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
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01502999
FRAMEWORK SYSTEM
RAHMENSYSTEM
SYSTEME DE STRUCTURE
PATENT ASSIGNEE:
   Future System Consulting Corporation, (4157550), Shibuya Shin-minamiguchi
      Bldg, 3-28-13 Shibuya, Shibuya-ku, Tokyo 150-0002, (JP), (Applicant
      designated States: all)
INVENTOR:
   ISHIBASHI, K. c/o Future System Consulting Corp., Shibuya
      Shin-minamiguchi Bldg., 3-28-13, Shibuya, Shibuya-ku, Tokyo 150-0002,
      (JP)
   MAESHIMA, M, c/o Future System Consulting Corp., Shibuya Shin-minamiguchi
      Bldg., 3-28-13, Shibuya, Shibuya-ku, Tokyo 150-0002, (JP)
   OKUMURA, N., c/o Future System Consulting Corp., Shibuya Shin-minamiguchi
      Bldg., 3-28-13, Shibuya, Shibuya-ku, Tokyo 150-0002, (JP)
   SAKASHITA, Isao ,c/o Future System Consulting Corp, Shibuya
      Shin-minamiguchi Bldg., 3-28-13, Shibuya, Shibuya-ku, Tokyo 150-0002,
      (JP)
   IGAKURA, Yoko, c/o Future System Consulting Corp., Shibuya
      Shin-minamiguchi Bldg., 3-28-13, Shibuya, Shibuya-ku, Tokyo 150-0002,
      (JP)
LEGAL REPRESENTATIVE:
   Hoffmann, Eckart, Dipl.-Ing. (5571), Patentanwalt, Bahnhofstrasse 103,
      82166 Grafelfing, (DE)
PATENT (CC, No, Kind, Date): EP 1347390 A1 030924 (Basic)
                              WO 2002054263 020711
APPLICATION (CC, No, Date):   EP 2001995024 011227; WO 2001JP11532 011227
PRIORITY (CC, No, Date): JP 2000401794 001228
DESIGNATED STATES: GB
INTERNATIONAL PATENT CLASS (V7): G06F-015/00; G06F-015/16
ABSTRACT WORD COUNT: 223
NOTE:
   Figure number on first page: 12

LANGUAGE (Publication,Procedural,Application): English; English; Japanese
FULLTEXT AVAILABILITY:
Available Text   Language   Update   Word Count
      CLAIMS A   (English)   200339   2899
      SPEC A   (English)   200339   11986
Total word count - document A   14885
Total word count - document B   0
Total word count - documents A + B   14885
...SPECIFICATION messaging service(s) 15 might place such message(s) in
   message queue(s) employing nonvolatile **disk** storage (hereinafter "DB
   (database) **queue** (s)") and might continue to **store** such **message** (s)
   in DB **queue** (s) after such message(s) has or have been sent.
   Accordingly, in the case of...

...G," the same message(s) can be resent in the event of bad transmission,
   system **failure** , or other such problem.
      "Priority or priorities" indicates priority level(s) with which such
   message...

20/3,K/8   (Item 8 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
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01357759
**Storage system making possible data synchronisation confirmation at time of
   asynchronous remote copy**
**Speichersystem   zur Bestatigung der Datensynchronisierung wahrend des
   asynchronen Fernkopierens**
**Systeme de stockage avec confirmation de la synchronisation des donnees au
   moment de la replication des donnees distantes et asynchrones**
PATENT ASSIGNEE:
   Hitachi, Ltd., (204145), 6 Kanda Surugadai 4-chome, Chiyoda-ku,Tokyo
      100-8010, (JP), (Applicant designated States: all)
INVENTOR:
   Urabe, Kiichiro,c/o Hitachi, Ltd.,Intel. Prop., Group,New Marunouchi
      Bldg.5-1 Marunouchi 1-chome, Chiyoda-ku,Tokyo 100-8220, (JP)
   Uratani, Ikuo,c/o Hitachi, Ltd.,Int. Prop. Group, New Marunouchi
      Bldg.,5-1 Marunouchi 1-chome, Chiyoda-ku,Tokyo 100-8220, (JP)
LEGAL REPRESENTATIVE:
   Strehl Schubel-Hopf·& Partner (100941), Maximilianstrasse 54, 80538
      Munchen, (DE)
PATENT (CC, No, Kind, Date):   EP 1158409 A2   011128 (Basic)
                               EP 1158409 A3   061011
APPLICATION (CC, No, Date):   EP 2000120301 000915;
PRIORITY (CC, No, Date): JP 2000159547 000525
DESIGNATED STATES: AT; BE; CH; CY; DE; DK; ES; FI; FR; GB; GR; IE; IT; LI;
   LU; MC; NL; PT; SE
EXTENDED DESIGNATED STATES: AL; LT; LV; MK; RO; SI
INTERNATIONAL PATENT CLASS (V7): G06F-011/14; G06F-011/20
INTERNATIONAL CLASSIFICATION (V8 + ATTRIBUTES):
IPC + Level Value Position Status Version Action Source Office:
   G06F-0011/14   A I F B 20060101 20010921 H EP
   G06F-0011/20   A I L B 20060101 20010921 H EP
ABSTRACT WORD COUNT: 109
NOTE:
   Figure number on first page: 1

LANGUAGE (Publication,Procedural,Application): English; English; English
FULLTEXT AVAILABILITY:
Available Text   Language   Update   Word Count
      CLAIMS A   (English)   200148   1173
      SPEC A   (English)   200148   9158
Total word count - document A   10333

Total word count - document B   0
Total word count - documents A + B   10333
...SPECIFICATION host computer.
     In the case where asynchronous transfer cannot be conducted because of
   a transfer **fault** or the like of the transmission paths between the
   large-sized disk array devices 13a and 13b, the large-sized **disk** array
   devices 13a and 13b manage the **data** which have not yet been
   transferred and which are **queued** in the FIFOs 7 and 8, in the bit maps
   110 and 120 as difference data by taking a volume as the unit, and brings
   duplication into a **fault** suspension (PSUE) state.
     The middle software 3a of the host 1a checks the state of...

20/3,K/9   (Item 9 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
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01165614
**Extensible   distributed   enterprise application   integration   system and
   methods of operating same**
**System zum Integrieren von erweiterbaren verteilten Betriebsanwendungen und
   Verfahren zu dessen Betrieb**
**Systeme   pour   l'integration   d'applications   d'entreprise   extensibles   et
   distribuees et methode pour son exploitation**
PATENT ASSIGNEE:
  Saga Software Inc., (2908810), 11190 Sunrise Valley Drive, Reston, VA
     20191, (US), (Applicant designated States: all)
INVENTOR:
  Yee, Hon-Siew, 12830 Tewksbury Drive, Herndon VA 20171, (US)
  Gordon, Gary Alan, 1524 Lincoln Way No.413, McLean, VA 22102, (US)
  Taylor, John Timothy, 709 Southview Place, Leesburg VA 20176, (US)
LEGAL REPRESENTATIVE:
  Haley, Stephen (79721), Gill Jennings & Every, Broadgate House, 7 Eldon
     Street, London EC2M 7LH, (GB)
PATENT (CC, No, Kind, Date): EP 1016989 A2 000705 (Basic)
                              EP 1016989 A3 020102
APPLICATION (CC, No, Date):   EP 99309204 991118;
PRIORITY (CC, No, Date): US 108993 P 981118; US 412595 991005; US 412633
     991005
DESIGNATED STATES: AT; BE; CH; CY; DE; DK; ES; FI; FR; GB; GR; IE; IT; LI;
  LU; MC; NL; PT; SE
EXTENDED DESIGNATED STATES: AL; LT; LV; MK; RO; SI
INTERNATIONAL PATENT CLASS (V7): G06F-017/30; G06F-009/54
ABSTRACT WORD COUNT: 141
NOTE:
  Figure number on first page: 4C

LANGUAGE (Publication,Procedural,Application): English; English; English
FULLTEXT AVAILABILITY:
Available Text   Language   Update   Word Count
      CLAIMS A   (English)   200027   1549
      SPEC A   (English)   200027   23065
Total word count - document A   24614
Total word count - document B   0
Total word count - documents A + B   24614

...SPECIFICATION real time when the subscribing application is available.
  If the subscribing application is unavailable, the **message** is **stored**
  in a **persistent** **queue** for later delivery.
     To be effective, the message delivery vehicle must include a business
  transaction...

...must complete in order for the transaction to occur. If even one unit of
  work **fails** , the whole transaction **fails** , and all completed units of
  work must then be reversed. These transactions are long running...

20/3,K/10   (Item 10 from file: 348)

01054910
DATA UPDATING SCHEME AND DATA UPDATING METHOD
SCHEMA UND VERFAHREN ZUR DATENAKTUALISIERUNG
PROCEDE ET DISPOSITIF POUR LA MISE A JOUR DE DONNEES
PATENT ASSIGNEE:
   MITSUBISHI DENKI KABUSHIKI KAISHA, (208589), 2-3, Marunouchi 2-chome
      Chiyoda-ku, Tokyo 100-8310, (JP), (Applicant designated States: all)
INVENTOR:
   SAKAKURA, Takashi Mitsubishi Denki Kabushiki, Kaisha 2-3, Marunouchi
      2-chome, Chiyoda-ku Tokyo 100-8310, (JP)
LEGAL REPRESENTATIVE:
   Pfenning, Meinig & Partner GbR (100967), Mozartstrasse 17, 80336 Munchen,
      (DE)
PATENT (CC, No, Kind, Date): EP 952510 A1 991027 (Basic)
                              WO 9926165 990527
APPLICATION (CC, No, Date):   EP 98924554 980610; WO 98JP2550 980610
PRIORITY (CC, No, Date): JP 97313073 971114
DESIGNATED STATES: DE; FR; GB
INTERNATIONAL PATENT CLASS (V7): G06F-001/00; G06F-012/00
ABSTRACT WORD COUNT: 159
NOTE:
   Figure number on first page: 1

LANGUAGE (Publication,Procedural,Application): English; English; Japanese
FULLTEXT AVAILABILITY:
Available Text   Language   Update   Word Count
     CLAIMS A   (English)   9943   1643
     SPEC A   (English)   9943   9484
Total word count - document A   11127
Total word count - document B   0
Total word count - documents A + B   11127

...SPECIFICATION memory pool, re-using of the memory area is possible by an
   operation of the **queue** link pointer.
      Further, in step 905, a **content** of the memory pool is **written** to an
   update request storing **disk** 403 (one example of memory unit)
   corresponding to the memory pool of the update request...

...in the following section "6. Notification function to update content
   access user".
      When the system **fails** due to some trouble, the update request queue
   is restored to the memory 406 by...

20/3,K/11   (Item 11 from file: 348)

01031951
Log based data architecture for a transactional message queuing system
Auf   Logdaten-basierte   Architektur   fur   ein   transaktionelles
   Nachrichtenwarteschlangensystem
Architecture basee sur des donnees de journal pour un systeme de file
   d'attente des messages transactionnels
PATENT ASSIGNEE:
   MITSUBISHI DENKI KABUSHIKI KAISHA, (208589), 2-3, Marunouchi 2-chome,
      Chiyoda-ku, Tokyo 100-8310, (JP), (Applicant designated States: all)
INVENTOR:
   Wong, David W.H., 162 Guggins Lane, Boxborough, MA 01719, (US)

Schwenke, Derek L., 95 Rice Street, Marlboro, MA 01752, (US)
LEGAL REPRESENTATIVE:
  Pfenning, Meinig & Partner (100961), Mozartstrasse 17, 80336 Munchen,
  (DE)
PATENT (CC, No, Kind, Date): EP 918284 A2 990526 (Basic)
                              EP 918284 A3 020925
APPLICATION (CC, No, Date):   EP 98114062 980728;
PRIORITY (CC, No, Date): US 963188 971103
DESIGNATED STATES: AT; BE; CH; CY; DE; DK; ES; FI; FR; GB; GR; IE; IT; LI;
  LU; MC; NL; PT; SE
EXTENDED DESIGNATED STATES: AL; LT; LV; MK; RO; SI
INTERNATIONAL PATENT CLASS (V7): G06F-011/00; G06F-011/14; G06F-017/30;
  G06F-009/46
ABSTRACT WORD COUNT: 196
NOTE:
  Figure number on first page: 5

LANGUAGE (Publication,Procedural,Application): English; English; English
FULLTEXT AVAILABILITY:
Available Text Language   Update   Word Count
      CLAIMS A (English) 9921   323
      SPEC A   (English) 9921   5947
Total word count - document A   6270
Total word count - document B   0
Total word count - documents A + B   6270

...SPECIFICATION up front to avoid paying the heavier cost of doing
  extensive log record scans upon **recovery** . This tradeoff can be adjusted
  and fine-tuned to suit the application requirements and domains...

...data regarding the state of the individual queue entries, and pointer
  offsets to where on **disk** the **messages** are physically **stored** . The
  Queue Entry Map Table serves as a fixed checkpoint interval for the
  entire message queuing system...

 20/3,K/12   (Item 12 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
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00809204
A recoverable disk control system with a non-volatile memory
Wiederherstellbares Plattensteuersystem mit nichtfluchtigem Speicher
Systeme de commande de disque restaurable avec memoire remanente
PATENT ASSIGNEE:
  KABUSHIKI KAISHA TOSHIBA, (213130), 72, Horikawa-cho, Saiwai-ku,
    Kawasaki-shi, Kanagawa-ken 210-8572, (JP), (Proprietor designated
    states: all)
INVENTOR:
  Shimizu, Kuniyasu, c/o Toshiba Corp., Int. Prop. Div., 1-1-1 Shibaura,
    Minato-ku, Tokyo, (JP)
  Hirayama, Hideaki, c/o Toshiba Corp., Int. Prop. Div., 1-1-1 Shibaura,
    Minato-ku, Tokyo, (JP)
LEGAL REPRESENTATIVE:
  Shindler, Nigel (35821), Brookes Batchellor 102-108 Clerkenwell Road,
    London EC1M 5SA, (GB)
PATENT (CC, No, Kind, Date): EP 751462 A1 970102 (Basic)
                              EP 751462 B1 020731
APPLICATION (CC, No, Date):   EP 96302962 960426;
PRIORITY (CC, No, Date): JP 95151737 950619
DESIGNATED STATES: DE; FR; GB
INTERNATIONAL PATENT CLASS (V7): G06F-011/14
ABSTRACT WORD COUNT: 104
NOTE:
  Figure number on first page: NONE

...SPECIFICATION which the flag has not been set yet, through the control block of the definite **queue** (S110 in Figure 5(b)). The **disk write** means **stores** the **write data** in the **disk** according to the request structure 1 (S130 in Figure 5(b)). When the writing process...

...written in the disk device.
   Here, the operation of this invention is explained, when a **fault** occurs in the computer system, referring to Figures 6 - 8.
   As shown in Figure 8, a **disk** write request from the operating system **stores write** requests and **write data** A, B and C in the indefinite **queue** in the nonvolatile memory one by one (point 1(circle) in Figure 8). The operating...

...last checkpoint is executed after the next checkpoint. For instance, even if a power supply **failure** occurs in the computer system, the information of the write request is maintained because the...

...and the write data generated after the last checkpoint which are stored in the indefinite **queue** in the nonvolatile memory. However, the **data** in the definite **queue** is **written** into the **disk** device. Thus, even if the computer system is broken down by the power supply **failure** , the **write data** in the definite **queue** is **written** into the **disk** device at the reboot of the computer system.
   Concurrently with the processing of write requests can secure **fault** tolerance of the computer system. The disk control system of this invention can improve the...

00777497
Optical disc system
Optische Platteneinrichtung
Systeme de disque optique
PATENT ASSIGNEE:
  DISCOVISION ASSOCIATES, (260273), 2355 Main Street Suite 200, Irvine, CA
    92714, (US), (Proprietor designated states: all)
INVENTOR:
  Crupper, Randolph Scott, 308 High Street - PO Box 731,, Palmer Lake, CO
    80133, (US)
  Davis, Marvin Benjamin, 2813 Palmer Park Blvd., Colorado Springs, CO
    80909, (US)
  Getreuer, Kurt Walter, 5055 Horseshoe Bend,, Colorado Springs, CO 80917,
    (US)
  Grassens, Leonardus Johannes, 19115 Pebble Beach Way,, Monument, CO 80132
    , (US)
  Lewis, David Earl, 14820 Spiritwood Loop,, Black Forest, CO 80106, (US)
  Schell, David Louis, 5307 Borrego Drive, Colorado Springs, CO 80918, (US)
LEGAL REPRESENTATIVE:
  Bazzichelli, Alfredo et al (40161), c/o Societa Italiana Brevetti S.p.A.
    Piazza di Pietra, 39, 00186 Roma, (IT)
PATENT (CC, No, Kind, Date):   EP 726564 A1 960814 (Basic)
                               EP 726564 B1 030409
APPLICATION (CC, No, Date):   EP 96300350 960118;
PRIORITY (CC, No, Date): US 376882 950125
DESIGNATED STATES: AT; BE; CH; DE; ES; FR; GB; IE; IT; LI; NL; PT; SE
RELATED DIVISIONAL NUMBER(S) - PN (AN):
  EP 829859 (EP 97118097)
  EP 829952 (EP 97118101)
  EP 829858 (EP 97118093)
  EP 840301 (EP 97118096)
  EP 829860 (EP 97118098)
  EP 840309 (EP 97118099)
  EP 829869 (EP 97118100)
  EP 829861 (EP 97118102)
  EP 838811 (EP 97118094)
  EP 840300 (EP 97118095)
INTERNATIONAL PATENT CLASS (V7): G11B-007/09; G11B-007/085; G11B-007/095;
  G11B-007/135; G11B-011/10
ABSTRACT WORD COUNT: 259
NOTE:
  Figure number on first page: 5
LANGUAGE (Publication,Procedural,Application): English; English; English
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS A	(English)	EPAB96	3507
CLAIMS B	(English)	200315	455
CLAIMS B	(German)	200315	410
CLAIMS B	(French)	200315	523
SPEC A	(English)	EPAB96	90876
SPEC B	(English)	200315	88265
Total word count - document A			94403
Total word count - document B			89653
Total word count - documents A + B			184056

...SPECIFICATION isolator having a means for receiving a pole piece
  assembly for motion therewith and a **crash** stop for contacting
  structure moving relative to the isolator. In accordance with one
  embodiment of...

...carrying therein a portion of a structure to be protected within the

housing structure, a **crash** stop for contacting an object moving
relative to the isolator, or both such a shoe and **crash** stop. The shoe
according to this invention may include a compression rib provided
thereon for absorbing compression forces acting upon the shoe.
  The present optical **drive** system may alternatively be provided with a
mechanical isolator having a first means for mitigating...

 20/3,K/14   (Item 14 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
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00574050
Posted write disk array system.
Speicherplattenmatrize mit Nachschreibfahigkeit.
Systeme d'un reseau de disques avec post-ecriture.
PATENT ASSIGNEE:
  Compaq Computer Corporation, (687792), 20555 S.H. 249, Houston Texas
    77070, (US), (applicant designated states:
    AT;BE;CH;DE;DK;ES;FR;GB;IE;IT;LI;NL;PT;SE)
INVENTOR:
  Schneider, Randy D., 9214 Towerstone Drive, Spring, Texas 77379, (US)
  Flower, David L., 18807 Pheasant Lane, Tomball, Texas 77375, (US)
LEGAL REPRESENTATIVE:
  Brunner, Michael John et al (28871), GILL JENNINGS & EVERY Broadgate
    House 7 Eldon Street, London EC2M 7LH, (GB)
PATENT (CC, No, Kind, Date):   EP 573308 A2   931208 (Basic)
                               EP 573308 A3   940216
APPLICATION (CC, No, Date):   EP 93304371 930604;
PRIORITY (CC, No, Date): US 894067 920605
DESIGNATED STATES: AT; BE; CH; DE; DK; ES; FR; GB; IE; IT; LI; NL; PT; SE
INTERNATIONAL PATENT CLASS (V7): G06F-003/06; G06F-011/10; G06F-012/08;
ABSTRACT WORD COUNT: 145

LANGUAGE (Publication,Procedural,Application): English; English; English
FULLTEXT AVAILABILITY:
Available Text Language   Update   Word Count
    CLAIMS A (English)   EPABF1   2335
    SPEC A   (English)   EPABF1   19289
Total word count - document A   21624
Total word count - document B   0
Total word count - documents A + B   21624

...SPECIFICATION the respective drive queues to ensure the integrity of the
parity information with its corresponding **data** . A blocker **write**
merely reserves a place in the **drive   queue** , and no write operation
actually takes place.
    Mirrored writes include both normal writes and **mirrored** writes. The
normal writes are linked by next...

...ptr, as shown. The **mirrored** writes are linked to their corresponding
data writes by seq...needed to do so because the reads would have been
already sent to their respective **drive   queues** . Therefore, the
scheduler task examines the **data   write** requests in a parity **write**
request list prior to separating requests into their respective drive
queues.
    It is noted that these same concerns do not arise in **mirrored** write
request lists because the **mirrored** write requests are exact copies of
the data write requests. If a data write request in a **mirrored** write
request list was required to be delayed to allow a flush to complete, the
...

...data written to the data portion of the drive array A as well as the
mirrored portion. Thus, **mirrored** write requests in a **mirrored** write
request list are not examined prior to separating the requests into

individual drive queues...

...queues, the dequeue task and the post processor task execute the
requests in these respective **drive queues** to actually perform the
data transfers and status updates to accomplish the reads and writes.
The scheduler task determines if the...

...the drive array and includes data guard operations, such as a parity
write or a **mirrored** write. The scheduler task also marks the
destination of each of the requests. Read hits...requests will be either
the host writes in a parity write request list or the **mirrored** writes
in a **mirrored** write request list, and returns to step 302 to again
traverse through the above steps...

...steps are skipped. The recursion is aimed at executing step 484 once
more on nonposted **mirrored** writes and twice more on nonposted parity
writes to send the host **data writes** and the parity or **mirrored**
writes to their respective individual **drive queues** . If the request
lists contain only simple reads or simple writes, then no recursion takes
...

20/3,K/15   (Item 15 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
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00448487
REMOTE CONFINEMENT SYSTEM
HAUSARRESTSYSTEM
SYSTEME D'EMPRISONNEMENT A DISTANCE
PATENT ASSIGNEE:
  BI, Inc., (1600910), Boulder, Colorado, (US), (applicant designated
    states: AT;BE;DE;FR;GB;IT)
INVENTOR:
  WILLIAMSON, Lon, A., 6576 Tylersville Road, West Chester, OH 45069, (US)
  PENNYPACKER, Frank, C., 743 Wards Corner Road, Loveland, OH 45140, (US)
  COLLIER, Donald, W. 6101 Sheridan Road E., Apartment 41A, Chicago, IL
    60601, (US)
  FULLER, Kip, L., 5559 South Washington Street, Littleton, CO 80121, (US)
LEGAL REPRESENTATIVE:
  Allen, Oliver John Richard et al (27641), Lloyd Wise, Tregear & Co.,
    Commonwealth House, 1-19 New Oxford Street, London WC1A 1LW, (GB)
PATENT (CC, No, Kind, Date): EP 474701 A1 920318 (Basic)
                              EP 474701 B1 960710
                              WO 9013197 901101
APPLICATION (CC, No, Date):   EP 90908342 900426; WO 90US2297 900426
PRIORITY (CC, No, Date): US 343860 890426
DESIGNATED STATES: AT; BE; DE; FR; GB; IT
INTERNATIONAL PATENT CLASS (V7): H04M-011/00; G07C-009/00;
NOTE:
  No A-document published by EPO
LANGUAGE (Publication,Procedural,Application): English; English; English
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	EPAB96	1574
CLAIMS B	(German)	EPAB96	1412
CLAIMS B	(French)	EPAB96	1715
SPEC B	(English)	EPAB96	13343
Total word count - document A			0
Total word count - document B			18044
Total word count - documents A + B			18044

...SPECIFICATION therein will be transmitted. The transmission will
  include the HMU ID number stored in the **eprom** 86, the event time and
  event type **stored** in the **message queue** 60, and such other

information as the system designer may assign. Once this message is...

...will be awaited for another call in accordance with the same logic
previously described. The **failure** of the host 27 to reply could
indicate a busy condition at the host unit...

20/3,K/16   (Item 16 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
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00368108
**Queued posted-write disk write method and apparatus with improved error
    handling**
**Warteschlangen-Nachschreibplattenschreibverfahren   und   -gerät   mit
    Fehlerbehandlung**
**Procede et dispositif de mise en file d'attente d'ecriture sur disque a
    ecriture posterieure a traitement d'erreur**
PATENT ASSIGNEE:
  Compaq Computer Corporation, (687792), 20555 S.H. 249, Houston Texas
      77070, (US), (applicant designated states:
      BE;CH;DE;ES;FR;GB;GR;IT;LI;NL;SE)
INVENTOR:
  Jones, Curtis R., 12822 Chriswood Drive, Cypress Texas 77429, (US)
  Gready, Robert S., 14150 Wunderlick No. 2004, Houston Texas 77069, (US)
LEGAL REPRESENTATIVE:
  Brunner, Michael John et al (28871), GILL JENNINGS & EVERY Broadgate
      House 7 Eldon Street, London EC2M 7LH, (GB)
PATENT (CC, No, Kind, Date):   EP 359384 A2 900321 (Basic)
                               EP 359384 A3 910710
                               EP 359384 B1 980114
APPLICATION (CC, No, Date):   EP 89307948 890804;
PRIORITY (CC, No, Date): US 245865 880916
DESIGNATED STATES: BE; CH; DE; ES; FR; GB; GR; IT; LI; NL; SE
INTERNATIONAL PATENT CLASS (V7): G06F-011/00;
ABSTRACT WORD COUNT: 115

LANGUAGE (Publication,Procedural,Application): English; English; English
FULLTEXT AVAILABILITY:
Available Text Language   Update   Word Count
      CLAIMS B (English) 9803   413
      CLAIMS B (German)   9803   382
      CLAIMS B (French)   9803   465
      SPEC B   (English) 9803   2643
Total word count - document A   0
Total word count - document B   3903
Total word count - documents A + B   3903

...ABSTRACT information sectors to disk sectors includes error-handling
    routines to minimize the risk of data **loss** upon specified types of
    errors. Upon timeouts, queuing is suspended and all information sectors
    pending...

...discontinued and repeated attempts are made to write out all information
    sectors to the corresponding **disk** sectors. For each unsuccessful
    attempt, the corresponding **information** sector is **saved** in the **queue**
    ; the user is alerted, and subsequent read or write requests directed to
    the corresponding **disk** sector is serviced from the **saved** **information**
    sector in the **queue.**

...SPECIFICATION occurrence of a timeout or upon occurrence of a specified
    type of error, to which **queue** may be added information sectors,
    containing data to be stored on a disk, addressed to...

...and from which queue the information sectors may be written out to the

respective corresponding **disk** sectors,
the method comprising the steps of:
blocking the addition of information sectors to the...

...added to the queue.
The invention further provides an apparatus for maintaining a
posted-write **queue** as defined in claim 5.
A posted- **write** queuing program for **writing** **information** sectors to
disk sectors includes error-handling routines to minimize the risk of
data loss upon specified types...
...discontinued and repeated attempts are made to write out all information
sectors to the corresponding **disk** sectors. For each unsuccessful
attempt, the corresponding **information** sector is **saved** in the **queue**
; the user is alerted, and subsequent read or write requests directed to
the corresponding **disk** sector is serviced from the **saved** **information**
sector in the **queue** .

Posted- **Write** Queueing in **Disk** Cache Utility
The present invention is illustrated by the following description of a
set of...

20/3,K/17   (Item 17 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
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·00367854
**Queue-based inter-process communications system for digital data processing
system**
**Auf Warteschlangen basiertes Interprozess-Kommunikationssystem fur ein
digitales Datenverarbeitungssystem**
**Systeme de communication entre des processus base sur des files d'attente
pour un systeme numerique de traitement de donnees**
PATENT ASSIGNEE:
  DIGITAL EQUIPMENT CORPORATION, (313081), 111 Powdermill Road, Maynard
    Massachusetts 01754-1418, (US), (applicant designated states:
    AT;BE;CH;DE;ES;FR;GB;GR;IT;LI;LU;NL;SE)
INVENTOR:
  Mann, Bruce, Valley Road, Mason New Hampshire 03048, (US)
  Rosen, Michael, Dale Street, Wilton New Hampshire 03086, (US)
  Baradine, Pamela M., 19 Anna Louise Drive, Hudson New Hampshire, (US)
LEGAL REPRESENTATIVE:
  Goodman, Christopher et al (31122), Eric Potter & Clarkson St. Mary's
    Court St. Mary's Gate, Nottingham NG1 1LE, (GB)
PATENT (CC, No, Kind, Date):   EP 353079 A2 900131 (Basic)
                               EP 353079 A3 911218
                               EP 353079 B1 980415
APPLICATION (CC, No, Date):   EP 89307678 890727;
PRIORITY (CC, No, Date): US 226254 880729
DESIGNATED STATES: AT; BE; CH; DE; ES; FR; GB; GR; IT; LI; LU; NL; SE
INTERNATIONAL PATENT CLASS (V7): G06F-011/14; G06F-009/44;
ABSTRACT WORD COUNT: 127

LANGUAGE (Publication,Procedural,Application): English; English; English
FULLTEXT AVAILABILITY:
Available Text Language   Update   Word Count
      CLAIMS B (English) 9816   559
      CLAIMS B (German)   9816   474
      CLAIMS B (French)   9816   591
      SPEC B   (English) 9816   5036
Total word count - document A   0
Total word count - document B   6660
Total word count - documents A + B 6660

...SPECIFICATION to be stored in non-volatile memory (not shown) in the

computer system. If the **queue** 21 and associated **data** structures are not **stored** in **non - volatile memory** , a power **failure** can erase them. If the queue manager 17 is to periodically store, in **backup** storage such as a disk storage unit in the digital data processing system, the contents...

20/3,K/18   (Item 18 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
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00321666
Fault-tolerant, error-correcting storage system and method for storing digital information in such a storage system
Gegen Fehler tolerantes, Fehler korrigierendes Speichersystem und Verfahren zur Speicherung digitaler Information in einem derartigen Speichersystem
Systeme de memoire a correction d'erreur tolerant des fautes et methode a stocker d'information numerique dans un tel systeme de memoire
PATENT ASSIGNEE:
  STORAGE COMPUTER CORPORATION, (2170170), 11 Riverside Street, Nashua, New Hampshire 03062, (US), (Proprietor designated states: all)
INVENTOR:
  Goodlander, Theodore Jay, 4 Victoria Drive, Nashua New Hampshire 03063, (US)
LEGAL REPRESENTATIVE:
  Maury, Richard Philip et al (52807), Sommerville & Rushton, Business Link Building, 45 Grosvenor Road, St. Albans, Herts AL1 3AW, (GB)
PATENT (CC, No, Kind, Date):   EP 294287 A2 881207 (Basic)
                               EP 294287 A3 910424
                               EP 294287 B1 001129
APPLICATION (CC, No, Date):   EP 88401327 880601;
PRIORITY (CC, No, Date): US 57347 870602
DESIGNATED STATES: AT; BE; CH; DE; ES; FR; GB; GR; IT; LI; LU; NL; SE
INTERNATIONAL PATENT CLASS (V7): G11B-020/18; G06F-011/10
ABSTRACT WORD COUNT: 238

LANGUAGE (Publication,Procedural,Application): English; English; English
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	200048	3088
CLAIMS B	(German)	200048	2689
CLAIMS B	(French)	200048	3880
SPEC B	(English)	200048	8078
Total word count - document A			0
Total word count - document B			17735
Total word count - documents A + B			17735

...SPECIFICATION more host processors and one or more storage control units and storage units, such as **disk** devices. The cache mechanism includes a cache **store** for **storing data** to be read from or **written** to the storage units and a command **queue** for storing commands waiting execution by the **disk** devices. The cache mechanism executes commands received from the host processors in an order determined...
...ability to: reconstruct lost data through the use of one extra storage device containing error/ **recovery** code bits;
    have a single controller concurrently read and write to multiple storage devices;
    easily...

20/3,K/19   (Item 19 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT
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01152963   **Image available**
EMAIL USING QUEUES IN NON-PERSISTENT MEMORY
COURRIER ELECTRONIQUE UTILISANT DES FILES D'ATTENTE DANS UNE MEMOIRE NON
    PERSISTANTE
Patent Applicant/Assignee:
  STRONGMAIL SYSTEMS INC, 11965 Venice Boulevard, Suite 209, Los Angeles,
    CA 90066, US, US (Residence), US (Nationality), (For all designated
    states except: US)
Patent Applicant/Inventor:
  ADDANTE Frank, 11965 Venice Boulevard, Suite 209, Los Angeles, CA 90066,
    US, US (Residence), US (Nationality), (Designated only for: US)
  MCQUILLEN Tim, 11965 Venice Boulevard, Suite 209, Los Angeles, CA 90066,
    US, US (Residence), US (Nationality), (Designated only for: US)
Legal Representative:
  HARRIS Scott C (agent), Fish & Richardson P.C., 12390 El Camino Real, San
    Diego, CA 92130, US,
Patent and Priority Information (Country, Number, Date):
  Patent: WO 200475007 A2-A3 20040902 (WO 0475007)
  Application: WO 2004US4305 20040211 (PCT/WO US04004305)
  Priority Application: US 2003449301 20030220
Parent Application/Grant:
  Related by Continuation to: US 2003449301 20030220 (CIP)
Designated States:
(All protection types applied unless otherwise stated - for applications
2004+)
  AE AG AL AM AT AU AZ BA BB BG BR BW BY BZ CA CH CN CO CR CU CZ DE DK DM
  DZ EC EE EG ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC
  LK LR LS LT LU LV MA MD MG MK MN MW MX MZ NA NI NO NZ OM PG PH PL PT RO
  RU SC SD SE SG SK SL SY TJ TM TN TR TT TZ UA UG US (patent) UZ VC VN YU
  ZA ZM ZW
  (EP) AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HU IE IT LU MC NL PT RO SE
  SI SK TR
  (OA) BF BJ CF CG CI CM GA GN GQ GW ML MR NE SN TD TG
  (AP) BW GH GM KE LS MW MZ SD SL SZ TZ UG ZM ZW
  (EA) AM AZ BY KG KZ MD RU TJ TM
Publication Language: English
Filing Language: English
Fulltext Word Count: 7261

Fulltext Availability:
  Detailed Description

Detailed Description
... mail may be assigned with a unique ID formed from a bit vector
 of the **queued** e-mail. The **bit** vector may be **stored** in
 persistent storage . The **bit** vector may include sufficient
 information to reconstruct the message and the state of
 processing of...

...of the e-mails. This information is stored
 on disk or persistent storage. This enables **recovering** the
 entire state of processing of the system, without storing the
 entirety of the e...

 20/3,K/20   (Item 20 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT
(c) 2006 WIPO/Thomson. All rts. reserv.

01139516   **Image available**
DIRECT MEMORY ACCESS CONTROLLER SYSTEM
SYSTEME DE DISPOSITIF DE COMMANDE DE L'ACCES DIRECT EN MEMOIRE
Patent Applicant/Assignee:
  EMULEX CORPORATION, 3333 Susan Street, Costa Mesa, CA 92626, US, US
    (Residence), US (Nationality)

Inventor(s):
  CLAYTON Shawn Adam, 1186 Main Street, Boylston, MA 01505, US,
  FORTIN Brian Mark, 50 Laurel Drive, Hudson, MA 01749, US,
  WILLIE Daniel Brian, 9092 Fieldcrest Lane, Longmont, CO 80503, US,
  WOOD John Leland, 29 Raeder Drive, Stratham, NH 03885, US,
Legal Representative:
  KUBOTA Glenn M (agent), Morrison & Foerster LLP, 555 West Fifth Street,
    Suite 3500, Los Angeles, CA 90013-1024, US,
Patent and Priority Information (Country, Number, Date):
  Patent: WO 200461687 A1 20040722 (WO 0461687)
  Application: WO 2003US39583 20031211 (PCT/WO US03039583)
  Priority Application: US 2002324310 20021219
Designated States:
(Protection type is "patent" unless otherwise stated - for applications
prior to 2004)
  CA JP KR
  (EP) AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HU IE IT LU MC NL PT RO SE
SI SK TR
Publication Language: English
Filing Language: English
Fulltext Word Count: 10885

Fulltext Availability:
  Detailed Description

Detailed Description
... interleaving EDC
  calculations, as described below.

  [00133] Failed DMA Operation
  [001341 If a DMA operation **fails** for some, reason,, the DMA
  descriptor 600 will be left at the head of the...

...the DMA controller 102 is re-started, the
  DMA controller 102 will re-try the **failed** DMA operation.

  [001361 Skipping the **failed** DMA operation may require
   firmware executed by the CPU to "fix" the **message** **queue** head
  pointer **stored** in the **message** module 204 to skip the
  descriptor 600 of the **failed** operation, deal with any credit
  updates that may be required, and then re-start the DMA
  controller 102. These error **recovery** operations may occur
  even as new descriptors 600 are being added to the message
  queues...

 20/3,K/21   (Item 21 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT
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01098221   **Image available**
INSTALLING SOFTWARE APPLICATIONS AND ASSOCIATED DATA ON MOBILE COMPUTERS
INSTALLATION D'APPLICATIONS LOGICIELLES ET DE DONNEES ASSOCIEES SUR DES
    ORDINATEURS PORTABLES
Patent Applicant/Assignee:
  SAP AKTIENGESELLSCHAFT, Intellectual Property Department, Neurottstrasse
    16, 69190 Walldorf, DE, DE (Residence), DE (Nationality)
Inventor(s):
  HENIG Gerhard, Alstater Str. 64, 69124 Heidelberg, DE,
  KOMPALLI Prasad, 607 A-Block Natasha Golf View, Bangalore 560071, Domlur,
    IN,
Legal Representative: —
  SCHIUMA Daniele (et al) (agent), Muller-Bore & Partner, Grafinger Str. 2,
    61671 Munchen, DE,
Patent and Priority Information (Country, Number, Date):

Designated States:
(Protection type is "patent" unless otherwise stated - for applications
prior to 2004)
  AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CO CR CU CZ DE DK DM DZ
  EC EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR
  LS LT LU LV MA MD MG MK MN MW MX MZ NI NO NZ OM PG PH PL PT RO RU SC SD
  SE SG SK SL SY TJ TM TN TR TT TZ UA UG UZ VC VN YU ZA ZM ZW
  (EP) AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HU IE IT LU MC NL PT RO SE
  SI SK TR
  (OA) BF BJ CF CG CI CM GA GN GQ GW ML MR NE SN TD TG
  (AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZM ZW
  (EA) AM AZ BY KG KZ MD RU TJ TM
Publication Language: English
Filing Language: English
Fulltext Word Count: 3746

Fulltext Availability:
  Detailed Description

Detailed Description
... specific data 44 are extracted from CDB
  20 and the data are posted in outbound **queue** 26.

  Installation control computer 16, at 128, **backs - up**
  common **data** 42 in **non - volatile** **memory** 88 to **back - up** server
  86. Installation control computer 16 determines at 130
  whether the application installation was completed...

 20/3,K/22   (Item 22 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT
(c) 2006 WIPO/Thomson. All rts. reserv.

01097672
FLEXIBLE DATA TRANSFER AND DATA SYNCRONIZATION
TRANSFERT ET SYNCHRONISATION DE DONNEES SOUPLE
Patent Applicant/Assignee:
  GRUINTINE PUECHE INC, 3621 Estate View Street, Las Vegas, NE 89129, US,
    US (Residence), US (Nationality)
Inventor(s):
  ELDER Kevin, 3621 Estate View Street, Las Vegas, NV 89129, US,
  HEMMERS Oliver, 5370 Angler Circle #102, Las Vegas, NV 89122, US,
Legal Representative:
  MALLIE Michael J (et al) (agent), Blakely, Sokoloff, Taylor & Zafman LLP,
    12400 Wilshire Boulevard, 7th Floor, Los Angeles, CA 90025, US,
Patent and Priority Information (Country, Number, Date):
Designated States:
(Protection type is "patent" unless otherwise stated - for applications
prior to 2004)
  AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CO CR CU CZ DE DK DM DZ
  EC EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR
  LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ OM PH PL PT RO RU SD SE SG SK
  SL TJ TM TN TR TT TZ UA UG UZ VN YU ZA ZM ZW
  (EP) AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HU IE IT LU MC NL PT RO SE
  SI SK TR
  (OA) BF BJ CF CG CI CM GA GN GQ GW ML MR NE SN TD TG
  (AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZM ZW
  (EA) AM AZ BY KG KZ MD RU TJ TM
Publication Language: English

Filing Language: English
Fulltext Word Count: 8831

Fulltext Availability:
  Detailed Description

Detailed Description
... that allow the use of the entire bandwidth available to the system, or
   adjust for **faulty** and congested network communications by rerouti,ng
   the data across different networks among other examples. It also allows
   for easily sharing information between multiple **mirrored** partners, thus
   providing a high degree of flexibility.

   (0030] Once the DTA 250 has control...
...250
   connects to the DRA 270 of the remote data storage system and begins the
   mirroring or synchronization process.

   [0031] It should be understood that in one embodiment, the invention
   achieves...

...asynchronous method by queuing the file system events as they occur, and
   not trying to **queue** the raw **data** as it is **written** to **disk** . This
   method gives this embodiment of the invention the capability of being
   able to look...

...to the file or directory is the only information that is required to
   facilitate the **mirroring** process.

   [0032] It should also be understood that due to the complete separation
   of the...

 20/3,K/23   (Item 23 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT
(c) 2006 WIPO/Thomson. All rts. reserv.

01082686   **Image available**
**APPARATUS AND METHOD FOR INPUT OF IDEOGRAPHIC KOREAN SYLLABLES FROM REDUCED
    KEYBOARD**
**APPAREIL ET PROCEDE PERMETTANT DE SAISIR DES SYLLABES IDEOGRAPHIQUES
    COREENNES AVEC UN CLAVIER REDUIT**
Patent Applicant/Assignee:
  2012244 ONTARIO INC, 295 Phillip Street, Waterloo, Ontario N2L 3W8, CA,
    CA (Residence), CA (Nationality), (For all designated states except:
    US)
Patent Applicant/Inventor:
  FUX Vadim, 575 Rustic Drive, Waterloo, Ontario N2K 2A4, CA, CA
    (Residence), IL (Nationality), (Designated only for: US)
Legal Representative:
  PATHIYAL Krishna K (et al) (agent), Research In Motion Limited, 295
    Phillip Street, Waterloo, Ontario N2L 3W8, CA,
Patent and Priority Information (Country, Number, Date):
  Patent: WO 200406079 A2-A3 20040115 (WO 0406079)
  Application: WO 2003CA1025 20030703   (PCT/WO CA03001025)
  Priority Application: US 2002393997 20020703; US 2002282518 20021029; CA
    2410057 20021029
Designated States:
(Protection type is "patent" unless otherwise stated - for applications
prior to 2004)
  AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CO CR CU CZ DE DK DM DZ
  EC EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR
  LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ OM PH PL PT RO RU SC SD SE SG
  SK SL TJ TM TN TR TT TZ UA UG US UZ VC VN YU ZA ZM ZW
  (EP) AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HU IE IT LU MC NL PT RO SE

SI SK TR
  (OA) BF BJ CF CG CI CM GA GN GQ GW ML MR NE SN TD TG
  (AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZM ZW
  (EA) AM AZ BY KG KZ MD RU TJ TM
Publication Language: English
Filing Language: English
Fulltext Word Count: 11977

Fulltext Availability:
  Detailed Description

Detailed Description
... either volatile or non-volatile, although non-volatile memory such as
  Flash RAM or battery **backed - up** RAM is preferred when any data in the
  writeable memory should be maintained in the event of **loss** of power.
  The 30 above correspondence arrays, the Hangul syllable frequency data
  structure, and any...

...or adaptation of frequency of use information, then frequency data
  structures are preferably stored in **non - volatile** writeable **memory** .
  Otherwise, frequency of use **information** may instead be **stored** in **ROM**
  .

  The input **queue** 152, although shown as a separate block in Fig. 21,
  may actually be provided in...

 20/3,K/24   (Item 24 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT
(c) 2006 WIPO/Thomson. All rts. reserv.

01050140
DATA REPLICATION SYSTEM AND METHOD
SYSTEME ET PROCEDE DE REPRODUCTION DE DONNEES
Patent Applicant/Assignee:
  SHINKURO INC, 5110 Edgemoor Lane, Bethesda, MD 20814, US, US (Residence),
    US (Nationality)
Inventor(s):
  CROCKER Steve, 5110 Edgemoor Lane, Bethesda, MD 20814, US,
  KAY Jeffrey, 8333 Magic Leaf Road, Springfield, VA 22153, US,
Legal Representative:
  ROBERTS Jon L (et al) (agent), Roberts, Abokhair & Mardula, LLC, Suite
    1000, 11800 Sunrise Valley Drive, Reston, VA 20191, US,
Patent and Priority Information (Country, Number, Date):
  Patent: WO 200379213 A1 **20030925** (WO 0379213)
  Application: WO 2003US8036 20030317 (PCT/WO US0308036)
  Priority Application: US 2002364648 20020315; US 2002382659 20020522; US
    2003443239 20030128
Designated States:
(Protection type is "patent" unless otherwise stated - for applications
prior to 2004)
  AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CO CR CU CZ DE DK DM DZ
  EC EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR
  LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ OM PH PL PT RO RU SD SE SG SK
  SL TJ TM TN TR TT TZ UA UG UZ VN YU ZA ZM ZW
  (EP) AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HU IE IT LU MC NL PT RO SE
  SI SK TR
  (OA) BF BJ CF CG CI CM GA GN GQ GW ML MR NE SN TD TG
  (AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZM ZW
  (EA) AM AZ BY KG KZ MD RU TJ TM
Publication Language: English
Filing Language: English
Fulltext Word Count: 9685

Patent and Priority Information (Country, Number, Date):

Patent: ... **20030925**
Fulltext Availability:
  Detailed Description
Publication Year: **2003**

Detailed Description
... are processed by a background thread that cycles through the inbound
   queue periodically. Messages that **fail** processing are held in the queue
   for retry.
   [00601 Inbound messages can be retained in...

...queue's persistent store, allowing the POP3 client module 112 to
   asynchronously retrieve and post **messages** . The **queue** 's **persistent**
   store serves as an excellent **backup** mechanism should the client system
    fail . But most important, the queue helps manage out-

 20/3,K/25   (Item 25 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT
(c) 2006 WIPO/Thomson. All rts. reserv.

01035153   **Image available**
METHOD AND APPARATUS FOR REAL TIME STORAGE OF DATA NETWORKING BIT STREAMS
PROCEDE ET DISPOSITIF DE STOCKAGE EN TEMPS REEL DE TRAINS DE BITS DE
   RESEAUX DE DONNEES
Patent Applicant/Assignee:
  DIGITAL SOFTWARE CORPORATION, P.O. Box 590, Naperville, IL 60566-0590, US
   , US (Residence), US (Nationality)
Inventor(s):
  WOLFE Paul Kenneth Jr, 25W221 Highview Drive, Naperville, IL 60563, US,
Legal Representative:
  KRUEGER James P (et al) (agent), Fitch, Even, Tabin & Flannery, Suite
    1600, 120 South LaSalle Street, Chicago, IL 60603, US,
Patent and Priority Information (Country, Number, Date):
  Patent: WO 200365189 A1 **20030807** (WO 0365189)
  Application: WO 2003US2346 20030124 (PCT/WO US03002346)
  Priority Application: US 2002352514 20020131; US 2003347173 20030117
Designated States:
(Protection type is "patent" unless otherwise stated - for applications
prior to 2004)
  AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CO CR CU CZ DE DK DM DZ
  EC EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ LC LK LR
  LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ OM PH PL PT RO RU SC SD SE SG
  SK SL TJ TM TN TR TT TZ UA UG UZ VC VN YU ZA ZM ZW
  (EP) AT BE BG CH CY CZ DE DK EE ES FI FR GB GR HU IE IT LU MC NL PT SE SI
  SK TR
  (OA) BF BJ CF CG CI CM GA GN GQ GW ML MR NE SN TD TG
  (AP) GH GM KE LS MW MZ SD SL SZ TZ.UG ZM ZW
  (EA) AM AZ BY KG KZ MD RU TJ TM
Publication Language: English
Filing Language: English
Fulltext Word Count: 3941

Patent and Priority Information (Country, Number, Date):
  Patent: ... **20030807**
Fulltext Availability:
  Detailed Description
Publication Year: **2003**

Detailed Description
... storage, which is
   described in detail at www ibm.com/chips/index.html.

  Advantageously, a **mirroring** subsystem 40 may be connected
  to the hard **disk** drive circular queue 30. **Mirroring** Subsystem 40

transfers the **data** **stored** in the hard disk drive circular **queue** 30 to an auxiliary storage system 42. Auxiliary storage system 42 could be a Network **Mirroring** Subsystem 40 operates in accordance with software which reorders the sequence of **data** **stored** in the hard **disk** **drive** circular **queue** 30. In use, hard **disk** **drive** 31 contains the sequence of data items 1, 5, 9, 13 ... ; hard disk drive 32...

Set	Items	Description
S1	318852	QUEUE? ? OR FIFO OR FIRST(1X)FIRST OR BUFFER? ?
S2	35888	(VOLATILE OR (NON OR "NOT")()PERSISTENT)(5N)(STRUCTURE? ? - OR MEMORY OR STORE OR STORAGE)
S3	33950	(NON()VOLATILE OR PERSIST???)(5N)(S1 OR STRUCTURE? ? OR ME-MORY OR STORE OR STORAGE)
S4	138522	(READ()ONLY OR FLASH)()MEMORY OR ROM OR PROM OR EPROM OR E-EPROM OR FPO OR EDO OR FIRMWARE
S5	58051	(COPY??? OR COPIE? ? OR REPLICAT? OR DUPLICAT? OR CLONE? ? OR CLONING)(5N)(MESSAGE? ? OR PACKET? ? OR FRAME? ? OR DATA OR INFORMATION OR CONTENT? ? OR BYTE? ? OR BIT? ? OR SEQUENCE? ? OR CHARACTER? ? OR STRING? ? OR WORD? ?)
S6	40	S5(7N)S1(7N)S3:S4
S7	1	PN=US 20050060374
S8	1	S6 AND S7
S9	40	S6
S10	19	S9 AND AC=US/PR AND AY=(1963:2003)/PR
S11	24	S9 AND AC=US AND AY=1963:2003
S12	24	S9 AND AC=US AND AY=(1963:2003)/PR
S13	22	S9 AND PY=1963:2003
S14	31	S10:S13
S15	8839	MIRROR?(5N)(MESSAGE? ? OR PACKET? ? OR FRAME? ? OR DATA OR INFORMATION OR CONTENT? ? OR BYTE? ? OR BIT? ? OR SEQUENCE? ? OR CHARACTER? ? OR STRING? ? OR WORD? ?)
S16	47	(S5 OR S15)(10N)S1(10N)S3:S4
S17	7	S16 NOT S6
S18	87	QUEUE? ?(10X)(COPY??? OR COPIE? ? OR REPLICAT? OR DUPLICAT? OR CLONE? ? OR CLONING OR MIRROR?)(10X)QUEUE? ?
S19	53	(NON()VOLATILE OR PERSIST???)(7N)QUEUE? ?
S20	3	S18 AND S19
S21	10677	(BACKUP OR BACK???()UP)(5N)(MESSAGE? ? OR PACKET? ? OR FRA-ME? ? OR DATA OR INFORMATION OR CONTENT? ? OR BYTE? ? OR BIT? ? OR CHARACTER? ? OR STRING? ? OR WORD? ?)
S22	11	S21(10N)S1(10N)S3:S4
S23	11	S22 NOT (S6 OR S20)
S24	24	QUEUE? ?(10X)(BACKUP OR BACK???()UP)(10X)QUEUE? ?
S25	0	S19 AND S24
S26	6	S19(10N)(COPY??? OR COPIE? ? OR REPLICAT? OR DUPLICAT? OR -CLON??? OR MIRROR? OR BACKUP OR BACK???()UP)
S27	362082	(TRANSFER????? OR WRITE? ? OR WRITING OR WRITTEN OR SAV???-)(5N)(MESSAGE? ? OR PACKET? ? OR FRAME? ? OR DATA OR INFORMAT-ION OR CONTENT? ? OR BYTE? ? OR BIT? ? OR CHARACTER? ? OR STR-ING? ? OR WORD? ?)
S28	735	S27(10N)S1(10N)S3:S4
S29	1115526	RECOVER??? OR RESTOR? OR CRASH??? OR FAIL??? OR FAULT? ? OR MIRROR? OR BACKUP OR BACK???()UP OR MIDDLEWARE
S30	93	S28 AND S29
S31	158	(INBOUND OR INCOMING OR OUTBOUND OR OUTGOING OR WORKING OR BOUND)(1W)QUEUE? ?
S32	0	S28 AND S31
S33	5	S19(10N)S27
S34	328	(NON()VOLATILE OR PERSIST???)(7N)(BUFFER? ? OR FIFO OR FIR-ST(1X)FIRST)
S35	80	S27(10N)S34
S36	19	S29 AND S35
S37	18	S36 NOT (S6 OR S20)
S38	5	S5(10N)S34
S39	21337	(S5 OR S15 OR S21 OR S27)(5N)S1:S2
S40	922	S39(7N)(DRIVE OR DISK? ? OR DISC? ?)
S41	191	S40 AND (S29 OR LOSS)

S42   6   S41 AND QUEUE? ?

14/3,K/1   (Item 1 from file: 347)

07582761   **Image available**
LOG INFORMATION COLLECTING SYSTEM AND METHOD FOR FLASH MEMORY

PUB. NO.:   2003-076604 [JP 2003076604 A]
PUBLISHED: March 14, 2003 (**20030314)**
INVENTOR(s):   AMAMIYA YOSHIYUKI
APPLICANT(s): NEC ACCESS TECHNICA LTD
APPL. NO.:   2001-265989 [JP 2001265989]
FILED: September 03, 2001 (20010903)

...PUBLISHED: **20030314)**

ABSTRACT
... idling (executing idle task). The control part 11 records the log
information in a temporary **buffer** 154 if the erasing flag 152 is on when
the event of recording the log **information** has occurred, and **copies** the
contents of the temporary **buffer** 154 in the **flash** **memory** during
idling.

14/3,K/2   (Item 2 from file: 347)

05042316   **Image available**
CD-ROM DUPLICATING DEVICE

PUB. NO.:   07-334916 [JP 7334916 A]
PUBLISHED: December 22, 1995 (**19951222)**
INVENTOR(s):   TAMURA TAIJI
APPLICANT(s): RICOH CO LTD [000674] (A Japanese Company or Corporation), JP
               (Japan)
APPL. NO.:   06-127432 [JP 94127432]
FILED: June 09, 1994 (19940609)

...PUBLISHED: **19951222)**

ABSTRACT
...CD-R drives 260. Each time a certain quantity of data is read from CD-
ROM and stored in the **buffer** , the data is written in each CD-R. When
there is not **duplication** prohibiting **information** in a master disk in
this CD- **ROM** duplicating device, the master disk is forcibly ejected by an
eject device.

14/3,K/3   (Item 3 from file: 347)

04829487   **Image available**
EEPROM ERROR CHECK SYSTEM

PUB. NO.:   07-122087 [JP 7122087 A]
PUBLISHED: May 12, 1995 (**19950512)**
INVENTOR(s):   SHIBUKAWA SHIGERU
               ECHIGO NOBUYUKI
APPLICANT(s): HITACHI LTD [000510] (A Japanese Company or Corporation), JP
               (Japan)
               HITACHI INSTR ENG CO LTD [485528] (A Japanese Company or

APPL. NO.: Corporation), JP (Japan)
05-266027 [JP 93266027]
FILED: October 25, 1993 (19931025)

...PUBLISHED: **19950512)**

ABSTRACT
... error detection bit and decides whether the error information is to be stored in a **buffer** 4 or not. Thus, the **duplication** of error **information** is avoided in the **EEPROM** during an data error detection, the error detection performance is improved, the correction process during ...

14/3,K/4 **(Item 4 from file: 347)**
DIALOG(R)File 347:JAPIO
(c) 2007 JPO & JAPIO. All rts. reserv.

04198137 **Image available**
ELECTRONIC DEVICE

PUB. NO.: 05-189837 [JP 5189837 A]
PUBLISHED: July 30, 1993 (**19930730)**
INVENTOR(s): MATSUMOTO YOSHIO
TANAKA MASATO
APPLICANT(s): SONY CORP [000218] (A Japanese Company or Corporation), JP (Japan)
APPL. NO.: 04-024375 [JP 9224375]
FILED: January 16, 1992 (19920116)
JOURNAL: Section: P, Section No. 1643, Vol. 17, No. 616, Pg. 135, November 12, 1993 (19931112)

...PUBLISHED: **19930730)**

ABSTRACT
...the power supply is on, the data is read and is then transferred to the **buffer** RAM 15. The **non - volatile memory** 16 has a front area 16A and a rear area 16B and can reduce the generation of **data** error through **duplicated** writing of the same **data** . Destruction of data in the **non - volatile memory** 16 can be lowered by **duplicated** writing of **data** and adverse effect on the apparatus due to destruction of data can also be prevented.

14/3,K/5 **(Item 5 from file: 347)**
DIALOG(R)File 347:JAPIO
(c) 2007 JPO & JAPIO. All rts. reserv.

04024797 **Image available**
PRINTER WITH PRESET LIST-PRINTING FUNCTION

PUB. NO.: 05-016497 [JP 5016497 A]
PUBLISHED: January 26, 1993 (**19930126)**
INVENTOR(s): HIBINO MASAAKI
APPLICANT(s): BROTHER IND LTD [000526] (A Japanese Company or Corporation), JP (Japan)
APPL. NO.: 03-167148 [JP 91167148]
FILED: July 08, 1991 (19910708)
JOURNAL: Section: M, Section No. 1422, Vol. 17, No. 286, Pg. 136, June 02, 1993 (19930602)

...PUBLISHED: **19930126)**

ABSTRACT
... are stored in RAM13. When printing of a preset list is conducted, the

printing control **data** in the **ROM** 12 are **copied** to a **character buffer** and codes for emphasizing characters are inserted before and behind a code part corresponding to...

14/3,K/6 **(Item 1 from file: 350)**
DIALOG(R)File 350:Derwent WPIX
(c) 2007 The Thomson Corporation. All rts. reserv.

0015035207 - Drawing available
WPI ACC NO: 2005-383198/200539
XRPX Acc No: N2005-310540
Solid state disk system has control module copying data segments listed in load priority queue from media to memory module and resuming copying, after copying all data segments in queue
Patent Assignee: HOLZMANN R (HOLZ-I)
Inventor: HOLZMANN R
Patent Family (1 patents, 1 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update	
US 20050097308	A1	20050505	US 2003697591	A	20031030	200539	B

Priority Applications (no., kind, date): US 2003697591 A 20031030

Patent Details

Number	Kind	Lan	Pg	Dwg	Filing Notes
US 20050097308	A1	EN	8	3	

200539
       ...NOVELTY - A control module **copies** **data** segments from **non - volatile storage** media to **memory** module, checks load priority **queue** , during start-up of disk system and stops **copying** of **data** segments listed in load map, if data segments are listed in load priority queue. The ...

Original Publication Data by Authority

Claims:
...load priority queue;said control module, if data segments are listed in said load priority **queue** , temporarily stopping said copying of data segments listed in said sequential load map;said control module **copying** the **data** segments listed in the load priority **queue** from the **non - volatile storage** media to said **memory** module;said control module, after all data segments in said load priority **queue** have been **copied** , resuming said **copying** of **data** segments listed in said sequential load map.

14/3,K/7 **(Item 2 from file: 350)**
DIALOG(R)File 350:Derwent WPIX
(c) 2007 The Thomson Corporation. All rts. reserv.

0015035200 - Drawing available
WPI ACC NO: 2005-383191/200539
XRPX Acc No: N2005-310533
Solid state disk system has control module to copy data segments updated in RAM module to non-volatile storage media, when data storage time interval expires
Patent Assignee: HOLZMANN R (HOLZ-I)
Inventor: HOLZMANN R
Patent Family (1 patents, 1 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update	
US 20050097288	A1	20050505	US 2003697590	A	20031030	200539	B

Priority Applications (no., kind, date): US 2003697590 A 20031030

Patent Details
Number   Kind Lan Pg Dwg Filing Notes
US 20050097288   A1   EN   8   3

200539
    ...a list of data segments that is updated in the RAM module. The control module copies the **data** segments listed in the segment **queue** to the **non - volatile storage** media such as semiconductor **memory**, when a data storage time interval expires.

Original Publication Data by Authority

Claims:
...have been updated in said memory module;said control module, when a time interval expires, **copying** the **data** segments listed in said segment **queue** to said **non - volatile storage** media.

   14/3,K/8   (Item 3 from file: 350)
DIALOG(R)File 350:Derwent WPIX
(c) 2007 The Thomson Corporation. All rts. reserv.

0014893382   - Drawing available
WPI ACC NO: 2005-241125/
XRPX Acc No: N2005-198749
Message e.g. edit and create message, **persistence enhancing method, involves** copying message **to working** queue **to** persist **message before message is removed from inbound** queue, **and processing message to generate reply**
Patent Assignee: INT BUSINESS MACHINES CORP (IBMC)
Inventor: PHILLIPS B R
Patent Family (1 patents, 1 countries)
Patent   Application
Number   Kind Date   Number   Kind   Date   Update
US 20050060374   A1   20050317 US 2003660289   A   20030911 200525 B

Priority Applications (no., kind, date): US 2003660289 A 20030911

Patent Details
Number   Kind Lan Pg Dwg Filing Notes
US 20050060374   A1   EN   12   4
Message e.g. edit and create message, **persistence enhancing method, involves** copying message **to working** queue **to** persist **message before message is removed from inbound** queue, **and processing message to generate reply**
    ...NOVELTY - The method involves browsing an inbound **queue** (135) to identify a **message**. The **message** is **copied** to a working **queue** to **persist** the message before the message is removed from the inbound **queue**, where the working **queue** is **persisted** by a **queue** manager. The message is locked until the **message** is **copied** to the working **queue**0. The **message** is processed to generate a reply prior to removing the message from the working queue.

Original Publication Data by Authority

Claims:
...1. A method for enhancing persistence of a message, the method comprising:browsing an inbound **queue** to identify the **message**; **copying** the **message** to a working **queue**, the working **queue** being **persisted** by a **queue** manager, to **persist** the message before the message is

removed from the inbound **queue** ; andprocessing the message to generate a
reply prior to removing the message from the...

14/3,K/9 **(Item 4 from file: 350)**
DIALOG(R)File 350:Derwent WPIX
(c) 2007 The Thomson Corporation. All rts. reserv.

0014883146 - Drawing available
WPI ACC NO: 2005-230885/200524
Related WPI Acc No: 2003-900400
XRPX Acc No: N2005-190031
**Meter data e.g. history logs, storing method for use in utility meter,
involves storing meter data in associated memory buffer, and copying meter**
data **into selected** flash memory **blocks of** non - volatile flash
memory   structures
Patent Assignee: ITRON ELECTRICITY METERING INC (ITRO-N); NORROD E
  (NORR-I); SEAL B K (SEAL-I); SIMMONS S M (SIMM-I)
Inventor: NORROD E; SEAL B; SEAL B K; SIMMONS S; SIMMONS S M
Patent Family (2 patents, 110 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update	
US 20050036387	A1	20050217	US 2002131605	A	20020424	200524	B
			US 2004949603	A	20040924		
WO 2006036650	A1	20060406	WO 2005US33606	A	20050920	200625	E

Priority Applications (no., kind, date): US 2002131605 A 20020424; US
  2004949603 A 20040924
Patent Details

Number	Kind	Lan	Pg	Dwg	Filing Notes
US 20050036387	A1	EN	11	4	C-I-P of application US 2002131605
					C-I-P of patent US 6798353
WO 2006036650	A1	EN			

National Designated States,Original: AE AG AL AM AT AU AZ BA BB BG BR BW
    BY BZ CA CH CN CO CR CU CZ DE DK DM DZ EC EE EG ES FI GB GD GE GH GM HR
    HU ID IL IN IS JP KE KG KM KP KR KZ LC LK LR LS LT LU LV LY MA MD MG MK
    MN MW MX MZ NA NG NI NO NZ OM PG PH PL PT RO RU SC SD SE SG SK SL SM SY
    TJ TM TN TR TT TZ UA UG US UZ VC VN YU ZA ZM ZW
Regional Designated States,Original: AT BE BG BW CH CY CZ DE DK EA EE ES
    FI FR GB GH GM GR HU IE IS IT KE LS LT LU LV MC MW MZ NA NL OA PL PT RO
    SD SE SI SK SL SZ TR TZ UG ZM ZW

200524...
...**use in utility meter, involves storing meter data in associated memory
buffer, and copying meter** data **into selected** flash memory **blocks of**
non - volatile flash memory   structures

14/3,K/10   **(Item 5 from file: 350)**
DIALOG(R)File 350:Derwent WPIX
(c) 2007 The Thomson Corporation. All rts. reserv.

0014649606 - Drawing available
WPI ACC NO: 2004-831625/
XRPX Acc No: N2004-657036
 Flash memory **device, has page** buffer **block registering** data **to be
accessible in** copy -**back operation and selection circuit differentiating
data groups to be copied back and to be initialized**
Patent Assignee: LEE J (LEEJ-I); SAMSUNG ELECTRONICS CO LTD (SMSU)
Inventor: LEE J; LEE J Y
Patent Family (3 patents, 3 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update	
US 20040221092	A1	20041104	US 2004830940	A	20040422	200482	B

```
JP 2004327021   A   20041118  JP 2004113843   A  20040408  200482  E
KR 2004093365   A   20041105  KR 200387633    A  20031204  200517  E
```

Priority Applications (no., kind, date): KR 200327230   A   20030429; KR
    200387633   A   20031204

Patent Details
```
Number          Kind  Lan   Pg  Dwg  Filing Notes
US 20040221092  A1    EN    15   10
JP 2004327021   A     JA    15
```
 Flash   memory **device, has page** buffer **block registering** data **to be
accessible in** copy -back operation and selection circuit differentiating
data groups to be copied back and to be...

 14/3,K/11   (Item 6 from file: 350)
DIALOG(R)File 350:Derwent WPIX
(c) 2007 The Thomson Corporation. All rts. reserv.

0014614349 - Drawing available
WPI ACC NO: 2004-796322/200478
Related WPI Acc No: 2004-796260; 2004-796261; 2004-796288; 2006-294031;
 2006-556912
XRPX Acc No: N2004-627704
**Object accessing method in network attached storage switch, involves
replicating object on primary file server to replica file server using
replication module to track new file location**
Patent Assignee: NEOPATH NETWORKS INC (NEOP-N)
Inventor: CHAWLA R; IYENGAR A; TSIRIGOTIS P; WONG T K
Patent Family (1 patents, 106 countries)
```
Patent                      Application
Number         Kind  Date   Number        Kind  Date      Update
WO 2004097686  A1    20041111  WO 2004US12846  A   20040426  200478  B
```

Priority Applications (no., kind, date): US 2004831701 A   20040423; US
    2003465578 P   20030424; US 2003465579 P   20030424; US 2004831376 A
    20040423

Patent Details
```
Number         Kind  Lan   Pg  Dwg  Filing Notes
WO 2004097686  A1    EN    37   8
```
National Designated States,Original: AE AG AL AM AT AU AZ BA BB BG BR BW
    BY BZ CA CH CN CO CR CU CZ DE DK DM DZ EC EE EG ES FI GB GD GE GH GM HR
    HU ID IL IN IS JP KE KG KP KR KZ LC LK LR LS LT LU LV MA MD MG MK MN MW
    MX MZ NA NI NO NZ OM PG PH PL PT RO RU SC SD SE SG SK SL SY TJ TM TN TR
    TT TZ UA UG US UZ VC VN YU ZA ZM ZW
Regional Designated States,Original: AT BE BG BW CH CY CZ DE DK EA EE ES
    FI FR GB GH GM GR HU IE IT KE LS LU MC MW MZ NA NL OA PL PT RO SD SE SI
    SK SL SZ TR TZ UG ZM ZW

Original Publication Data by Authority

Original Abstracts:
...file server interface (210), a replication module (220), and a
synchronization module (230) with a **persistent   buffer** (235). The file
server interface manages client requests before replication without
assistance. The replication module **replicates** a namespace separately from
 data contained therein. Afterwards, synchronization module looks-up the
switch file handle in a file handle...

 14/3,K/12   (Item 7 from file: 350)
DIALOG(R)File 350:Derwent WPIX

0014475736 - Drawing available
WPI ACC NO: 2004-667364/200465
XRPX Acc No: N2004-528569
Virtual memory management method e.g. for non volatile semiconductor memory, involves copying data stored in physical block to logical block if physical block is storing data in flash memory
Patent Assignee: HIGH TECH COMPUTER CORP (HIGH-N)
Inventor: CHIU Y
Patent Family (2 patents, 1 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update	
US 20040177214	A1	20040909	US 2003379444	A	20030303	200465	B
US 6928511	B2	20050809	US 2003379444	A	20030303	200552	E

Priority Applications (no., kind, date): US 2003379444 A 20030303

Patent Details

Number	Kind	Lan	Pg	Dwg	Filing Notes
US 20040177214	A1	EN	8	4	

200465·

Original Publication Data by Authority

Original Abstracts:
A method and system of managing virtual memory for a **flash - memory** system. Logical blocks in a **buffer** are used to store **data   copied** from a physical block in a **flash - memory** . An operating system searches data in the **buffer** first. If the data is in the buffer, it is accessed. If not, the operating...

...A method and system of managing virtual memory for a **flash - memory** system. Logical blocks in a **buffer** are used to store **data   copied** from a physical block in a **flash - memory** . An operating system searches data in the **buffer** first. If the data is in the buffer, it is accessed. If not, the operating...

 14/3,K/13   **(Item 8 from file: 350)**
DIALOG(R)File 350:Derwent WPIX

0014469735 - Drawing available
WPI ACC NO: 2004-661174/200464
Related WPI Acc No: 2005-329642; 2005-561711
XRPX Acc No: N2004-523515
Synchronous non - volatile flash memory **accessing method e.g. for electrically** EPROM **of computer system, involves** copying data **containing instruction code to** buffer **circuit during write operation**
Patent Assignee: MICRON TECHNOLOGY INC (MICR-N)
Inventor: ROOHPARVAR F F
Patent Family (2 patents, 1 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update	
US 20040172499	A1	20040902	US 2000627770	A	20000728	200464	B
			US 2004798065	A	20040311		
US 6920522	B2	20050719	US 2004798065	A	20040311	200547	E

Priority Applications (no., kind, date): US 2000627770 A 20000728; US 2004798065 A 20040311

Patent Details

Number	Kind	Lan	Pg	Dwg	Filing Notes	
US 20040172499	A1	EN	57	34	Division of application	US 2000627770

200464
Synchronous non – volatile flash memory **accessing method e.g. for electrically** EPROM **of computer system, involves** copying data **containing instruction code to** buffer **circuit during write operation**

Original Publication Data by Authority

Claims:
What is claimed is:1. A method of operating a **flash memory** comprising: **copying** first **data** stored in a row of a first array bank to a **buffer** circuit using control circuitry of the flash memory;performing a write operation to write second...

...1. A method of operating **a flash** memory **comprising** :copying **first** data stored in a row of a first array bank to **a** buffer circuit using control circuitry of the flash memory;performing a write operation to write ...

 14/3,K/14   **(Item 9 from file: 350)**
DIALOG(R)File 350:Derwent WPIX
(c) 2007 The Thomson Corporation. All rts. reserv.

0013938224 – Drawing available
WPI ACC NO: 2004-118430/
XRPX Acc No: N2004-094616
Distributed mapping scheme for flash memory, identifies empty memory location to write updated data and enters address of updated data into spare position of new data if spare location of old data is empty
Patent Assignee: ICREATE TECHNOLOGIES CORP (ICRE-N)
Inventor: OH Y; TUAN J C
Patent Family (1 patents, 1 countries)

Patent			Application				
Number	Kind	Date	Number	Kind	Date	Update	
US 6675281	B1	20040106	US 200254560	A	20020122	200412	B

Priority Applications (no., kind, date): US 200254560   A   20020122

Patent Details

Number	Kind	Lan	Pg	Dwg	Filing Notes
US 6675281	B1	EN	12	9	

    ...NOVELTY – The logical addresses of the **flash memory** are mapped to physical addresses using a mapping table. The old **data** in the **data** page is **copied** to a **buffer** , and the **buffer** is updated with new data. An empty memory location is identified to write the updated...

 14/3,K/15   **(Item 10 from file: 350)**
DIALOG(R)File 350:Derwent WPIX
(c) 2007 The Thomson Corporation. All rts. reserv.

0013634002 – Drawing available
WPI ACC NO: 2003-729696/
XRPX Acc No: N2003-583195
 Flash memory **for portable electronic device, has data multiplexers selecting transmitting path between page** buffers **and** data **bus for** duplication **of input** data
Patent Assignee: EMEMORY TECHNOLOGY INC (EMEM-N); LIWANG ELECTRONIC CO LTD
  (LIWA-N)

Inventor: LIN C; RIN K
Patent Family (2 patents, 2 countries)
Patent   Application
Number   Kind Date   Number   Kind Date   Update
US 6570809   B1   20030527   US 200263387   A   20020417 200369 B
JP 2004178620   A   20040624   JP 2002339889 A   20021122 200443 NCE

Priority Applications (no., kind, date): JP 2002339889 A 20021122; US
   200263387 A 20020417

Patent Details
Number   Kind Lan Pg Dwg Filing Notes
US 6570809   B1   EN   7   2
JP 2004178620   A   JA   11
 Flash memory **for portable electronic device, has data multiplexers
selecting transmitting path between page** buffers **and** data **bus for**
duplication **of input** data

   14/3,K/16   (Item 11 from file: 350)
DIALOG(R)File 350:Derwent WPIX
(c) 2007 The Thomson Corporation. All rts. reserv.

0013624302   - Drawing available
WPI ACC NO: 2003-719818/
XRPX Acc No: N2003-575381
**Engine control and diagnostic method e.g. in ground vehicles, involves
referencing buffer pointer locations in response to engine performance
trigger events and storing pre and post trigger performance parameters at
selected rate**
Patent Assignee: CUMMINS ENGINE CO INC (CUND)
Inventor: BAKER D R; GREENWOOD T E; MILVERT T J; SHUTTY J V; STOUGHTON R
Patent Family (1 patents, 1 countries)
Patent   Application
Number   Kind Date   Number   Kind Date   Update
US 6601015   B1   20030729   US 199876552   P   19980302 200368 B
                                     US 1999238854 A   19990128

Priority Applications (no., kind, date): US 199876552   P   19980302; US
   1999238854 A 19990128

Patent Details
Number   Kind Lan Pg   Dwg Filing Notes
US 6601015   B1   EN   14   5   Related to Provisional US 199876552
    ...a predetermined number of pre and post trigger engine performance
parameters are stored in the **buffer** at the selected rates. The **contents**
of the **buffer** are **copied** into **data** collection **memory** blocks (37) of
 non - volatile memory (34), for regulating the engine.

   14/3,K/17   (Item 12 from file: 350)
DIALOG(R)File 350:Derwent WPIX
(c) 2007 The Thomson Corporation. All rts. reserv.

0013568149   - Drawing available
WPI ACC NO: 2003-662477/ **200362**
XRPX Acc No: N2003-528739
**NAND flash memory for latching and storing data, has circuit to control
data loading circuit in response to column addresses in which pass data bit
is loaded to page buffer connected to defective column instead of program
bit**
Patent Assignee: LEE J (LEEJ-I); SAMSUNG ELECTRONICS CO LTD (SMSU)
Inventor: LEE J

Patent Family (8 patents, 5 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update	
US 20030133340	A1	20030717	US 2003340532	A	20030110	200362	B
DE 10301458	A1	20030814	DE 10301458	A	20030110	200362	E
JP 2003233995	A	20030822	JP 20032229	A	20030108	200364	E
KR 2003061877	A	20030723	KR 20021875	A	20020112	200381	E
KR 437461	B	20040623	KR 20021875	A	20020112	200470	E
US 6813184	B2	20041102	US 2003340532	A	20030110	200472	E
TW 200301903	A	20030716	TW 2003100504	A	20030110	200556	E
TW 231937	B1	20050501	TW 2003100504	A	20030110	200641	E

Priority Applications (no., kind, date): US 2003340532 A 20030110; KR 20021875 A 20020112

Patent Details

Number	Kind	Lan	Pg	Dwg	Filing Notes
US 20030133340	A1	EN	20	10	
JP 2003233995	A	JA	18		
KR 437461	B	KO			Previously issued patent KR 2003061877
TW 200301903	A	ZH			
TW 231937	B1	ZH			

200362

Alerting Abstract ...ADVANTAGE - The NAND **flash** **memory** enhances the efficiency of operations of programming, erasing and **copy** -back programming with loading pass **data** in page **buffers** corresponding to defective columns without the use of fuses...

14/3,K/18   **(Item 13 from file: 350)**
DIALOG(R)File 350:Derwent WPIX
(c) 2007 The Thomson Corporation. All rts. reserv.

0013328051 - Drawing available
WPI ACC NO: 2003-415416/
Related WPI Acc No: 2000-601069; 2003-557724
XRPX Acc No: N2003-330936
Updating method for ESCD in computer system, involves flashing updated data in primary volatile memory data buffer to data storage area in response to SMI, independent to operating mode of computer system
Patent Assignee: COMPAQ INFORMATION TECHNOLOGIES INC (COPQ)
Inventor: HESS R L; HOBSON L B; KANE J D; PIWONKA M A
Patent Family (1 patents, 1 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update	
US 6505278	B1	20030107	US 199870866	A	19980430	200339	B
			US 2000571467	A	20000516		

Priority Applications (no., kind, date): US 199870866 A 19980430; US 2000571467 A 20000516

Patent Details

Number	Kind	Lan	Pg	Dwg	Filing Notes
US 6505278	B1	EN	9	4	Continuation of application US 199870866
					Continuation of patent US 6073206

Original Publication Data by Authority

Claims:
...A method of updating data in a computer system, the method comprising

the steps of: **copying** **data** in a **data** **storage** area of a **non -** **volatile** **memory** of the computer system to a primary volatile memory data **buffer** ; detecting an update to the primary volatile memory data buffer; signaling a system management interrupt...

14/3,K/19 **(Item 14 from file: 350)**
DIALOG(R)File 350:Derwent WPIX
(c) 2007 The Thomson Corporation. All rts. reserv.

0013136775 - Drawing available
WPI ACC NO: 2003-219029/ **200321**
XRPX Acc No: N2003-174507
Remote computer operation monitoring method for business transaction, involves transmitting video transactions containing data snooped and compressed into encoded format to local computer
Patent Assignee: COMPAQ INFORMATION TECHNOLOGIES INC (COPQ)
Inventor: EMERSON T F; KRONTZ J; MICHAELS P J
Patent Family (1 patents, 1 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update	
US 6476854	B1	20021105	US 1996733254	A	19961018	200321	B

Priority Applications (no., kind, date): US 1996733254 A 19961018

Patent Details

Number	Kind	Lan	Pg	Dwg	Filing Notes
US 6476854	B1	EN	32	11	

200321
Original Publication Data by Authority

Original Abstracts:
...or a staleness timer times out. Special firmware executed in system management mode reads the **FIFO** and converts the video data and primitives into conventional ASCII text or the required format. The **firmware** also maintains a **copy** of the video **frame** **buffer** to further encode the video data, if possible. The **firmware** then transmits the conventional ASCII text via a modem to a user stationed at a...

14/3,K/20 **(Item 15 from file: 350)**
DIALOG(R)File 350:Derwent WPIX
(c) 2007 The Thomson Corporation. All rts. reserv.

0013132954 - Drawing available
WPI ACC NO: 2003-215197/
XRPX Acc No: N2003-171777
Memory access management apparatus for flash memory **has** copy buffer **which records** data **processing requirement on RAM for operation lists, and performs data processing in order**
Patent Assignee: SONY CORP (SONY)
Inventor: TERUYAMA K
Patent Family (1 patents, 1 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update	
JP 2003036201	A	20030207	JP 2001222349	A	20010724	200321	B

Priority Applications (no., kind, date): JP 2001222349 A 20010724

Patent Details

Number	Kind	Lan	Pg	Dwg	Filing Notes
JP 2003036201	A	JA	9	8...	

Memory access management apparatus for flash memory **has** copy buffer

which records data **processing requirement on RAM for operation lists, and performs data processing in order**
 ...NOVELTY - An access **buffer** (15) performs access to **flash memory** (11) or RAM (9) for data operation with respect to first **data** processing requirement. A **copy buffer** (17) records **data** precessing requirement on RAM for operation lists, and performs data processing in order with respect...

 14/3,K/21   (Item 16 from file: 350)
DIALOG(R)File 350:Derwent WPIX
(c) 2007 The Thomson Corporation. All rts. reserv.

0012914620 - Drawing available
WPI ACC NO: 2002-506453/ **200254**
XRPX Acc No: N2002-400633
Portable audio player e.g. MP3 player, plays audio data from flash EEPROM which stores copy of data in hard disk, when audio data reproduction is restarted
Patent Assignee: COMPAQ COMPUTER CORP (COPQ)
Inventor: BURROWS M
Patent Family (1 patents, 1 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update	
US 6377530	B1	20020423	US 1999249182	A	19990212	200254	B

Priority Applications (no., kind, date): US 1999249182 A 19990212

Patent Details

Number	Kind	Lan	Pg	Dwg	Filing Notes
US 6377530	B1	EN	11	5	

 200254
Original Publication Data by Authority

Claims:
...of operating an audio player, comprising:playing audio data stored in a first memory unit; **copying** a predefined amount of audio **data** from the first **memory** unit into a **non - volatile memory buffer** under predefined conditions before the audio player is powered down; andafter the audio player...

 14/3,K/22   (Item 17 from file: 350)
DIALOG(R)File 350:Derwent WPIX
(c) 2007 The Thomson Corporation. All rts. reserv.

0012764224 - Drawing available
WPI ACC NO: 2002-617859/
XRPX Acc No: N2002-489043
 EEPROM accessing method in network interface controller, involves copying contents of EEPROM **to** buffer **in host memory**
Patent Assignee: MINNICK L (MINN-I)
Inventor: MINNICK L
Patent Family (1 patents, 1 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update	
US 20020083150	A1	20020627	US 2000750234	A	20001227	200266	B

Priority Applications (no., kind, date): US 2000750234 A 20001227

Patent Details

Number	Kind	Lan	Pg	Dwg	Filing Notes

US 20020083150 A1 EN 6 2
 EEPROM **accessing method in network interface controller, involves**
copying contents of EEPROM **to** buffer **in host memory**

 14/3,K/23 **(Item 18 from file: 350)**
DIALOG(R)File 350:Derwent WPIX
(c) 2007 The Thomson Corporation. All rts. reserv.

0010832630 - Drawing available
WPI ACC NO: 2001-450214/ **200148**
XRPX Acc No: N2001-333209
Flashing method used in non-volatile memory of computer system, involves
flashing portion of non-volatile memory image stored in image buffer of
volatile memory during system management mode
Patent Assignee: COMPAQ COMPUTER CORP (COPQ)
Inventor: BROYLES P J; GIBBONS P L
Patent Family (1 patents, 1 countries)
Patent   Application
Number   Kind   Date   Number   Kind   Date   Update
US 6243809   B1   20010605 US 199870823   A   19980430 200148 B

Priority Applications (no., kind, date): US 199870823 A 19980430

Patent Details
Number   Kind Lan Pg Dwg Filing Notes
US 6243809   B1   EN   18   8

 200148
Original Publication Data by Authority

Original Abstracts:
...SMI handler code processes the SMI event code and calls SMI service
code, the image **buffer** is located by the SMI service code, and the SMI
service code **copies** **data** from the **non - volatile** **memory** to the
image **buffer.**

 14/3,K/24 **(Item 19 from file: 350)**
DIALOG(R)File 350:Derwent WPIX
(c) 2007 The Thomson Corporation. All rts. reserv.

0009923228
WPI ACC NO: 2000-223436/
Related WPI Acc No: 1997-332329
XRPX Acc No: N2000-167468
Location of a read only memory without relying on the memory containing a
predetermined signature in a personal computer by comparing data read in
successive reads
Patent Assignee: MICROSOFT CORP (MICT)
Inventor: LIPE R
Patent Family (1 patents, 1 countries)
Patent   Application
Number   Kind   Date   Number   Kind   Date   Update
US 6032273   A   20000229 US 1992844239   A   19920302 200019 B
                              US 1997798707   A   19970212

Priority Applications (no., kind, date): US 1992844239 A 19920302; US
   1997798707 A 19970212

Patent Details
Number   Kind Lan Pg Dwg Filing Notes
US 6032273   A   EN   11   2   Continuation of application US

Original Publication Data by Authority

Claims:
...if the memory was not determined in steps (a) and (b) to be occupied by
read only memory (**ROM**) or random access memory (RAM), **copying bytes** of the memory block into corresponding bytes in a memory **buffer** ;
(d) for each byte in the memory buffer, inverting each bit of the byte in
...

 14/3,K/25   **(Item 20 from file: 350)**
DIALOG(R)File 350:Derwent WPIX
(c) 2007 The Thomson Corporation. All rts. reserv.

0009173881 - Drawing available
WPI ACC NO: 1999-097073/ **199909**
XRPX Acc No: N1999-070580
**Method of data storage and recovery in control system, esp. in motor
vehicle, such as taxi - involves managing flash memory using ring storage
method whereby operating data of same type are acquired repeatedly: when
system is switched on newest data are read out to RAM memory**
Patent Assignee: SIEMENS AG (SIEI)
Inventor: DE W E; DE WILLE E; ULM M
Patent Family (6 patents, 5 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update	
DE 19740525	C1	19990204	DE 19740525	A	19970915	199909	B
FR 2768529	A1	19990319	FR 199811354	A	19980911	199918	E
GB 2330672	A	19990428	GB 199820021	A	19980914	199919	E
JP 11161563	A	19990618	JP 1998260316	A	19980914	199935	E
US 6167338	A	20001226	US 1998154152	A	19980915	200103	E
GB 2330672	B	20020724	GB 199820021	A	19980914	200256	E

Priority Applications (no., kind, date): DE 19740525 A 19970915
Patent Details

Number	Kind	Lan	Pg	Dwg	Filing Notes
DE 19740525	C1	DE	23	15	
JP 11161563	A	JA	17		

 199909

Original Publication Data by Authority

Original Abstracts:
...on again and which are stored in the RAM memory, are then stored in the
flash memory . The **flash memory** is managed by means of a circular
buffer method and can hold multiple **copies** of the operational **data** of
the same type. When the control system is switched on, the most recent
operational...

 14/3,K/26   **(Item 21 from file: 350)**
DIALOG(R)File 350:Derwent WPIX
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0008348761 - Drawing available
WPI ACC NO: 1997-462355/
XRPX Acc No: N1997-385064
**Synthetic filter for MPEG-2 audio decoder - has first and second
multiplexers and accumulators which are controlled by their respective**

controllers
Patent Assignee: KANKOKU DENKI TSUSHIN KOSHA (KANK-N); KOREA TELECOM
  (KOTE-N); KOREA TELECOM AUTHORITY (KOTE-N); KOREA TELECOM CORP (KOTE-N)
Inventor: HAN Y; HAN Y T; KAN E; KEN J; KO J; KO S; KOH J S; KWON S; KWON S.
  H

Patent Family (4 patents, 3 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update	
JP 9212486	A	19970815	JP 1996253333	A	19960925	199743	B
KR 1997019118	A	19970430	KR 199531605	A	19950925	199820	E
US 5812979	A	19980922	US 1996710677	A	19960923	199845	E
KR 147758	B1	19981201	KR 199531605	A	19950925	200031	E

Priority Applications (no., kind, date): KR 199531605   A 19950925

Patent Details

Number	Kind	Lan	Pg	Dwg	Filing Notes
JP 9212486	A	JA	6	3...	

Original Publication Data by Authority

Original Abstracts:
...queue memory by the cosine matrix coefficients stored in the first
coefficient ROM, a second **queue** memory for storing output data from the
first MAC unit therein, a third **queue** memory being **copied** with the
contents of the second **queue** memory, a second coefficient **ROM** for
storing window coefficients therein, a second MAC unit for synthesizing the
contents **copied** to the third **queue** memory with the window
coefficients stored in the second coefficient **ROM** to produce audio data,
an FIFO memory for storing the audio data from the second...

 14/3,K/27   **(Item 22 from file: 350)**
DIALOG(R)File 350:Derwent WPIX
(c) 2007 The Thomson Corporation. All rts. reserv.

0007703234 - Drawing available
WPI ACC NO: 1996-325603/ **199633**
XRPX Acc No: N1996-274114
**ROM filer e.g. EEPROM - returns stored data in data memory to end
indication in response to specific area of data memory**
Patent Assignee: NEC CORP (NIDE)
Inventor: ENDO K

Patent Family (4 patents, 3 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update	
JP 8147202	A	19960607	JP 1994288509	A	19941124	199633	B
CA 2162818	A	19960525	CA 2162818	A	19951114	199638	E
US 5933846	A	19990803	US 1995552290	A	19951102	199937	E
CA 2162818	C	19991019	CA 2162818	A	19951114	200009	E

Priority Applications (no., kind, date): JP 1994288509 A 19941124

Patent Details

Number	Kind	Lan	Pg	Dwg	Filing Notes
JP 8147202	A	JA	17	15	
CA 2162818	A	EN			
CA 2162818	C	EN			

 199633

Original Publication Data by Authority

Claims:
...means for receiving and detecting an access request to the rewritable area, and for a **copying** of a **data** stored in the rewritable area to the **buffer** in response to power being supplied to said **EPROM** file device which occurs prior to the access controller receiving and detecting a first access...

 14/3,K/28   (Item 23 from file: 350)
DIALOG(R)File 350:Derwent WPIX
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0006774535 - Drawing available
WPI ACC NO: 1994-159474/
XRPX Acc No: N1994-125377
Buffer backing store with ROM for storing frequently used words - has associated cache which supplies multiple words per clock to micro-bus on demand
Patent Assignee: INTEL CORP (ITLC)
Inventor: HUCK S; SHENOY S; SMITH F S
Patent Family (1 patents, 1 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update	
US 5313605	A	19940517	US 1990630534	A	19901220	199419	B

Priority Applications (no., kind, date): US 1990630534 A 19901220

Patent Details

Number	Kind	Lan	Pg	Dwg	Filing Notes
US 5313605	A	EN	13	8	

Original Publication Data by Authority

Original Abstracts:
...words. The buffer store has two parts, a cache RAM (64) and a two-word **queue** (62) comprised of two fetch **buffers** . The cache RAM is provided for storing a **copy** of some of the **word** stored in the backing store in accordance with a use algorithm. The **ROM** , **queue** **buffers** and cache RAM are simultaneously searched to see if the address for requested words is...

 14/3,K/29   (Item 24 from file: 350)
DIALOG(R)File 350:Derwent WPIX
(c) 2007 The Thomson Corporation. All rts. reserv.

0006591837 - Drawing available
WPI ACC NO: 1993-404307/ **199350**
XRPX Acc No: N1993-312922
Managing data records in non-volatile memory sub-system - involves using variable-length directory containing descriptors of disk records to locate selected record non-volatile memory
Patent Assignee: STORAGE TECHNOLOGY CORP (STOS)
Inventor: KURZAWA L J; PETERSON G W
Patent Family (1 patents, 1 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update	
US 5269019	A	19931207	US 1991681753	A	19910408	199350	B

Priority Applications (no., kind, date): US 1991681753 A 19910408

Patent Details

Number	Kind	Lan	Pg	Dwg	Filing Notes
US 5269019	A	EN	9	5	

199350

Claims:
...for said record descriptors from the list of free memory locations; (b)
writing, to said **data buffer** area, a **copy** of each said **data** record
which has been modified in both said **non - volatile memory** subsystem
and in said volatile memory subsystem after having been read into said
volatile memory...

 14/3,K/30 (Item 25 from file: 350)
DIALOG(R)File 350:Derwent WPIX
(c) 2007 The Thomson Corporation. All rts. reserv.

0006542096 - Drawing available
WPI ACC NO: 1993-351915/
XRPX Acc No: N1993-271420
Data record management in non-volatile memory - allocates buffer **area in**
non - volatile memory **, and writes to** buffer **area** copy **of** data
which has been modified since read into memory
Patent Assignee: STORAGE TECHNOLOGY CORP (STOS)
Inventor: KURZAWA L J; PETERSON G W
Patent Family (2 patents, 17 countries)

Patent Number	Kind	Date	Application Number	Kind	Date	Update	
WO 1993021579	A1	19931028	WO 1992US3299	A	19920421	199344	B
AU 199223357	A	19931118	AU 199223357	A	19920421	199410	E
			WO 1992US3299	A	19920421		

Priority Applications (no., kind, date): WO 1992US3299 A 19920421

Patent Details

Number	Kind	Lan	Pg	Dwg	Filing Notes
WO 1993021579	A1	EN	21	5	

National Designated States,Original: AU CA JP
Regional Designated States,Original: AT BE CH DE DK ES FR GB GR IT LU MC
    NL SE

AU 199223357	A	EN		PCT Application WO 1992US3299
				Based on OPI patent WO 1993021579 ..

...**allocates** buffer **area in** non - volatile memory **, and writes to**
buffer **area** copy **of** data **which has been modified since read into**
memory

  Alerting Abstract ...The method includes allocating a data **buffer** area
in a **non - volatile memory** subsystem for storing data records, and
writing to the **buffer** area a **copy** of the **data** record which has been
modified since having been read into the memory. A directory of...

 14/3,K/31 (Item 26 from file: 350)
DIALOG(R)File 350:Derwent WPIX
(c) 2007 The Thomson Corporation. All rts. reserv.

0006283440 - Drawing available
WPI ACC NO: 1993-076725/
XRPX Acc No: N1993-058932
Reducing lock period of shared buffer in data processing system - locking
buffer **as object before** content **of** buffer **is** copied **into memory, and**
then unlocking buffer **before** memory **is rendered** non - volatile
Patent Assignee: FUJITSU LTD (FUIT)
Inventor: HAYASHI K; MITANI M; SHIMOGAI Y
Patent Family (7 patents, 5 countries)

Patent Details

Number	Kind	Lan	Pg	Dwg	Filing Notes
WO 1993003436	A1	JA	35	8	

National Designated States,Original: JP US
Regional Designated States,Original: DE FR GB

EP 551528	A1	EN	17		PCT Application WO 1992JP996
					Based on OPI patent WO 1993003436

Regional Designated States,Original: DE FR GB

JP 05503476	X	JA			PCT Application WO 1992JP996
					Based on OPI patent WO 1993003436

EP 551528	A4	EN			
US 5715447	A	EN	15		Continuation of application WO

1992JP996

Continuation of application US

199339065

Continuation of application US

1995419055

EP 551528	B1	EN			PCT Application WO 1992JP996
					Based on OPI patent WO 1993003436

Regional Designated States,Original: DE FR GB

DE 69228297	E	DE			Application EP 1992916959
					PCT Application WO 1992JP996
					Based on OPI patent EP 551528
					Based on OPI patent WO 1993003436 ..

...locking buffer **as object before** content **of** buffer **is** copied **into memory, and then unlocking** buffer **before** memory **is rendered** non - volatile

   Alerting Abstract ...shared buffer (17) is rendered non-voltaile, the step (4) of locking temporarily the shared **buffer** as an object, the step (5) of **copying** the **content** of the **buffer** to the rendered **non - volatile** to the copy **memory** , the step (6) of unlocking the shared **buffer** after copying to the copy memory, and the step (8) of rendering the **content** of the **copy memory non - volatile** .